

SSCE5V081N7

4-line Ultra Low Capacitance Array for ESD Protection

Description

The SSCE5V081N7 is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines.

The SSCE5V081N7 has an ultra-low capacitance with a typical value at 0.7pF, and complies with the IEC 61000-4-2 (ESD) with ±15kV air and ±15kV contact discharge. It is assembled into a 10-pin 2.5x1.0x0.5mm lead-free DFN package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0 and HDMI. The small size, ultra-low capacitance and high ESD surge protection make SSCE5V081N7 an ideal choice to protect HDMI, MDDI, USB 3.0 and other high speed ports.

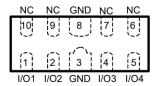
Feature

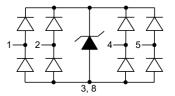
- \Rightarrow 56W peak pulse power (t_P = 8/20us)
- ♦ DFN2510-10L Package
- ♦ Working voltage: 5V
- ♦ Low clamping voltage
- Low capacitance
- ♦ Low leakage current
- ♦ RoHS compliant
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±15kV
Contact discharge: ±15kV

- IEC61000-4-5 (Surge) 8A (8/20us)

PIN configuration





Top view(Pin configuration)



Marking

Applications

- ♦ USB 2.0 and USB 3.1
- ♦ SATA and eSATA
- → DVI
- ♦ Portable Electronics and Notebooks
- ♦ HDMI 1.3, HDMI 1.4

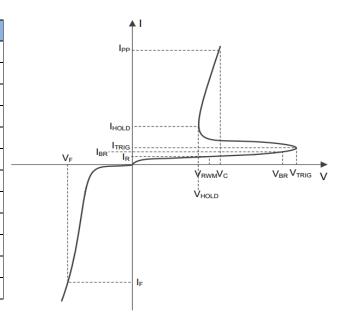
Mechanical data

- ♦ Lead finish:100% matte Sn (Tin)
- Mounting position: Any
- ♦ Qualified max reflow temperature:260 °C
- ♦ Device meets MSL 1 requirements
- ♦ Pure tin plating: 7 ~ 17 um
- ♦ Pin flatness: ≤3mil



• Electronic Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I _T		
I _T	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
V _{TRIG}	Reverse Trigger Voltage		
V _{TRIG}	Reverse Trigger Current		
VHOLD	Reverse Holding Voltage		
I _{HOLD}	Reverse Holding Current		
Сл	Junction Capacitance		



● Absolute maximum rating @TA=25℃

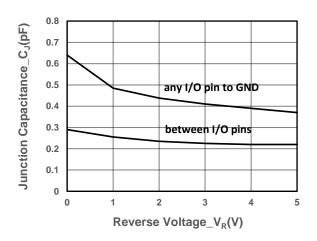
Parameter	Symbol	Value	Units		
Peak Pulse Power(t _P = 8/20us)	P _{PP}	56	W		
Peak Pulse Current (t _P = 8/20us)	I PP	8	Α		
ESD Rating per IEC61000-4-2:	Contact	V	15	KV	
	Air	V _{ESD}	15		
Storage Temperature		T _{STG}	-55/+150	$^{\circ}$	
Operating Temperature		TJ	-55/+125	${\mathbb C}$	

• Electrical Characteristics @TA=25℃

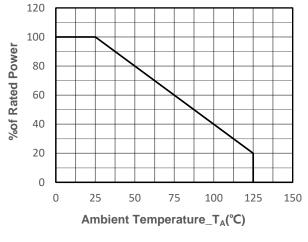
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}	Any I/O to GND			5	V
Breakdown Voltage	V_{BR}	$I_T = 1mA$ Any I/O to GND	6	7.5	9	V
Reverse Leakage Current	I _R	V _{RWM} =5V			0.1	μA
Forward Voltage	VF	I _F = 15mA		0.85	1.2	V
Clamping Voltage	Vc	$I_{PP} = 4A$, $t_P = 8/20 \mu s$		3		V
Clamping Voltage	Vc	I_{PP} =8A, t_P = 8/20 μ s		4	7	V
		V _R = 0V, f = 1MHz, between I/O pins		0.3	0.5	pF
Junction Capacitance	Сл	$V_R = 0V$, $f = 1MHz$, any I/O pin to GND		0.7	0.9	pF
		V _R = 2V, f = 1MHz, any I/O pin to GND		0.45	0.6	pF



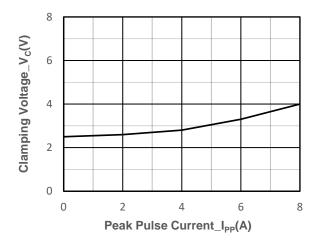
• Typical Performance Characteristics



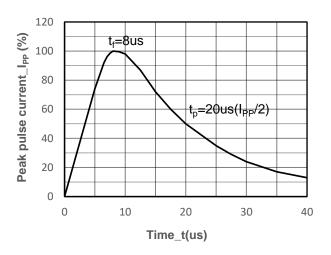
Junction Capacitance vs. Reverse Voltage



Power derating vs. Ambient temperature



Clamping Voltage vs. Peak Pulse Current



8/20us Pulse Waveform



• Package Information

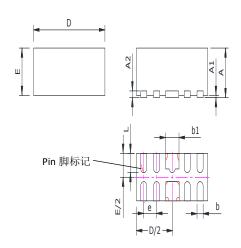
Ordering Information

Device	Package	Qty per Reel	Reel Size
SSCE5V081N7	DFN2510-10L	3000	7 Inch

Mechanical Data

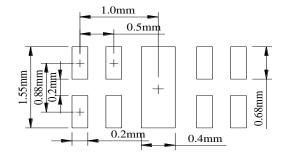
Case:DFN2510-10L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters			
DIIVI	Min	Max		
Α	0.45	0.65		
A 1	0.05REF			
A2	0.15REF			
b	0.15	0.25		
b1	0.30	0.50		
D	2.424	2.576		
E	0.924	1.076		
е	0.50REF			
L	0.30	0.45		

Recommended Pad outline





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