

SSCE3V382L1

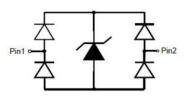
Ultra-low Capacitance Bi-directional Micro Packaged TVS Diodes for ESD Protection

Description

The SSCE3V382L1 is designed with SSC Punch-Through process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium. Also because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed, USB 4.0 super speed, VGA, DVI, HDMI, SDI and other high speed line applications.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), and EFT (electrical fast transients).

PIN configuration



DFN0603-2L



Feature

- \Rightarrow 34W peak pulse power (t_P = 8/20µs)
- ♦ DFN0603-2L Package
- ♦ Working voltage: 3.3V
- Low clamping voltage
- ♦ Low capacitance
- ♦ RoHS compliant
- Complies with following standards:
 - -IEC61000-4-2(ESD) ±15kV (contact), ±20kV(air)
 - -IEC61000-4-5 (Lightning)

4A (8/20µs)

Applications

- High Speed Line: USB1.0/2.0/3.0/4.0, VGA, DVI, SDI
- ♦ HDMI1.3/1.4/2.0
- Serial and Parallel Ports
- Notebooks, Desktops, Servers
- Cellular handsets and accessories
- Portable instrumentation
- Peripherals

Mechanical data

- ♦ Lead finish:100% matte Sn (Tin)
- ♦ Mounting position: Any
- ♦ Qualified max reflow temperature:260 °C

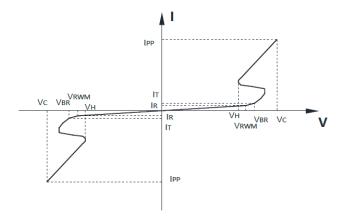
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- Device meets MSL 3 requirements
- ♦ Pure tin plating: 7 ~ 17 um
- ♦ Pin flatness: ≤3mil



• Electronic Parameter

Symbol	Parameter			
V _{RWM}	Peak Reverse Working Voltage			
I _R	Reverse Leakage Current @ V _{RWM}			
V_{BR}	Breakdown Voltage @ I _T			
Iτ	Test Current			
l _{PP}	Maximum Reverse Peak Pulse Current			
Vc	Clamping Voltage @ IPP			
P _{PP}	Peak Pulse Power			
С	Junction Capacitance			



Absolute maximum rating @T_A=25℃

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (8/20µs)	34	W
I PP	Peak Pulse Current (8/20µs)	4	Α
ESD Rating per IEC61000-4-2: Contact Air	V _{ESD}	15 20	kV
Тѕтс	Storage Temperature	-55/+150	$^{\circ}$
T _J	Operating Temperature	-55/+150	$^{\circ}\mathbb{C}$
TL	Lead Solder Temperature – Maximum (10 Second Duration)	260	$^{\circ}$

• Electrical Characteristics @T_A=25℃

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				3.3	V
Breakdown Voltage	V_{BR}	$I_T = 1mA$	7	10.5		V
Reverse Leakage Current	I _R	$V_{RWM} = 3.3V$		1	50	nA
Clamping Voltage	Vc	$I_{PP} = 1A$, $t_P = 8/20 \mu s$		4.5		V
Clamping Voltage	Vc	$I_{PP} = 4A, t_P = 8/20 \mu s$		5.5	7	V
Clamping Voltage ¹⁾	VC2	$I_{PP} = 16A, t_P = 100ns$		9		V
Dynamic resistance ²⁾	RDYN			0.25		Ω
Junction Capacitance	Сл	$V_R = 0V$, $f = 1MHz$		0.30	0.45	pF

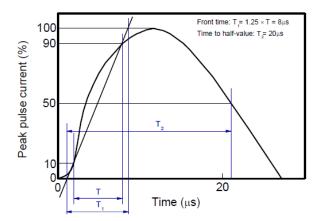
Notes:

1) TLP parameter: $Z0 = 50\Omega$, tp = 100ns, tr = 2ns, averaging window from 60ns to 80ns. RDYN is calculated from 4A to 16A.

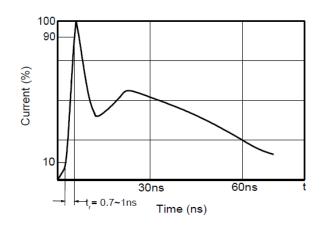
SSC-V1.1 <u>www.sscsemi.com</u> Analog Future



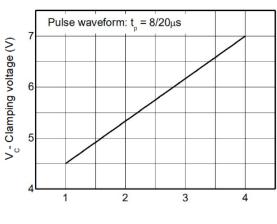
• Typical Performance Characteristics



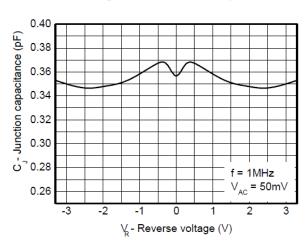
8/20µs waveform per IEC61000-4-5



Contact discharge current waveform per IEC61000-4-2

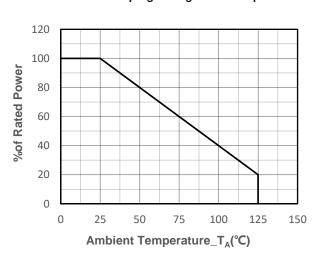


I_{PP} - Peak pulse current (A)

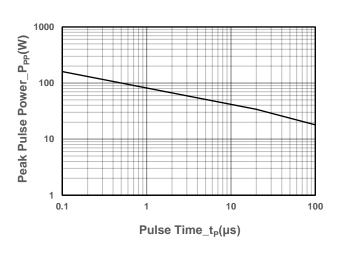


Capacitance vs. Reverse voltage

Clamping voltage vs. Peak pulse current



Power derating vs. Ambient temperature



Peak Pulse Power vs. Pulse Time



Package Information

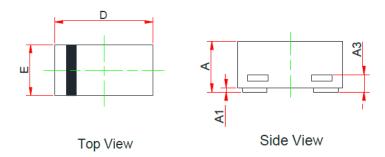
Ordering Information

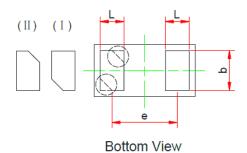
Device	Package	Qty per Reel	Reel Size
SSCE3V382L1	DFN0603-2L	15000	7 Inch

Mechanical Data

Case: DFN0603-2L

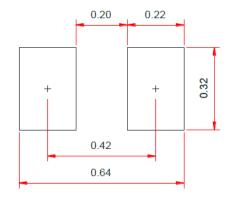
Case Material: Molded Plastic. UL Flammability





DIM	Millimeters				
DIN	Min	Тур	Max		
Α	0.230	-	0.340		
A 1	0.000	-	0.050		
А3	0.102REF				
D	0.550	0.600	0.670		
E	0.250	0.300	0.370		
b	0.215	-	0.295		
L	0.115	-	0.195		
е	0.40BSC				

Recommended Pad outline (Unit: mm)





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