



## SSC8LA8GN6

### N-Channel Enhanced MOSFET

#### ➤ Features

VDS	VGS	RDS(on) Typ.	ID
120V	±20V	6mΩ@10V	90A

#### ➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDS(on) with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. 100% UIS + DVDS Tested.

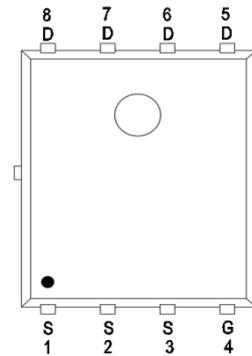
#### ➤ Applications

- DC/DC converters
- Power supplies
- Motor Drive Control
- Synchronous rectification

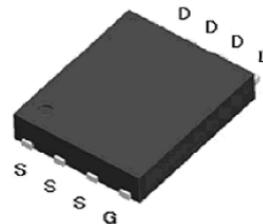
#### ➤ Ordering Information

Device	Package	Shipping
SSC8LA8GN6	PDFN5X6-8L	5000/Reel

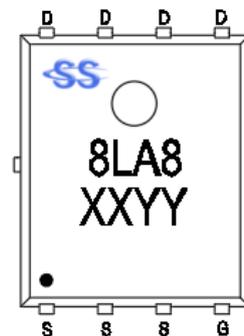
#### ➤ Pin configuration



Top View



PDFN5X6-8L



Marking

(XX: product year / YY: product week)

**➤ Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Unit	
$V_{DSS}$	Drain-to-Source Voltage	120	V	
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V	
$I_D$	Continuous Drain Current <sup>d</sup>	$T_C=25^{\circ}\text{C}$	91	A
		$T_C=100^{\circ}\text{C}$	45	
$I_{DSM}$	Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}\text{C}$	12	A
		$T_A=70^{\circ}\text{C}$	8	
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	360	A	
$P_D$	Power Dissipation <sup>c</sup>	$T_C=25^{\circ}\text{C}$	113	W
		$T_C=100^{\circ}\text{C}$	45	
$P_{DSM}$	Power Dissipation <sup>a</sup>	$T_A=25^{\circ}\text{C}$	2	W
		$T_A=70^{\circ}\text{C}$	1.28	
$I_{AS}$	Avalanche Current <sup>b</sup> $L=0.5\text{mH}$ Single Pulse	45	A	
$E_{AS}$	Avalanche Energy <sup>b</sup> $L=0.5\text{mH}$ Single Pulse	480	mJ	
$T_J$	Operation junction temperature	-55~150	$^{\circ}\text{C}$	
$T_{STG}$	Storage temperature range	-55~150		

**➤ Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	62.5	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	1.1	

Note:

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The value in any given application depends on the user is specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_D$  is based on  $T_{J(\text{MAX})}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

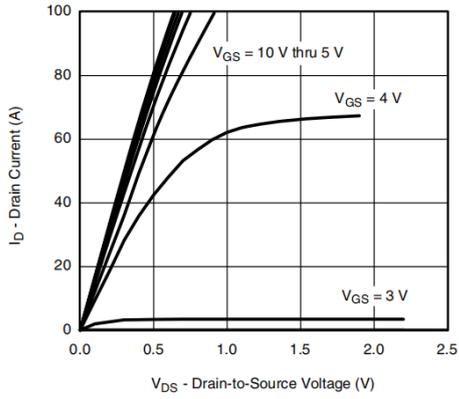


➤ **Electronics Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

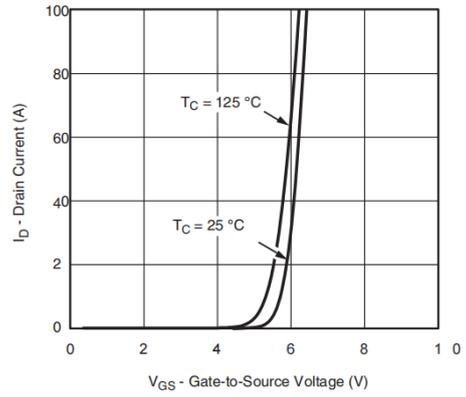
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	120			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	2.8	4	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		6	8.5	mΩ
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μA
I <sub>GSS</sub>	Gate-Source leak current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
G <sub>FS</sub>	Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		60		S
V <sub>SD</sub>	Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =20A		0.8	1.3	V
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, f=1MHz		2.1		R
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz		3700		pF
C <sub>oss</sub>	Output Capacitance			355		
C <sub>rss</sub>	Reverse Transfer Capacitance			17		
T <sub>D(ON)</sub>	Turn-on delay time	V <sub>GS</sub> =10V, R <sub>L</sub> =2.5R V <sub>DS</sub> =50V, R <sub>G</sub> =3R		22		ns
T <sub>r</sub>	Rise time			18		
T <sub>D(OFF)</sub>	Turn-off delay time			49		
T <sub>f</sub>	Fall time			19		
Q <sub>G</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V I <sub>D</sub> =20A		56		nC
Q <sub>GS</sub>	Gate Source Charge			12		
Q <sub>GD</sub>	Gate Drain Charge			14		
T <sub>rr</sub>	Diode Recovery Time	I <sub>F</sub> =20A, di/dt=200A/μs		66		ns
Q <sub>rr</sub>	Diode Recovery Charge	I <sub>F</sub> =20A, di/dt=200A/μs		102		nC



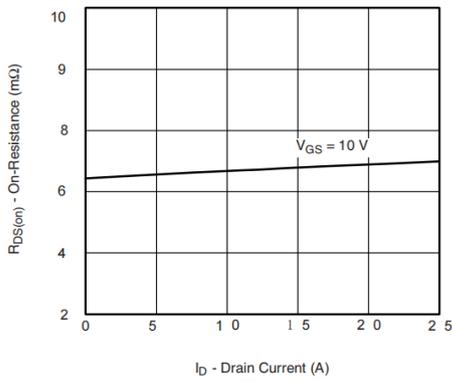
➤ Typical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)



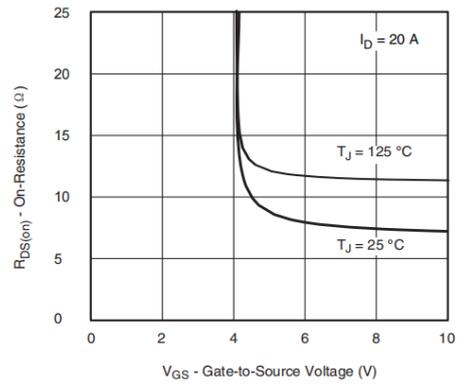
Output Characteristics



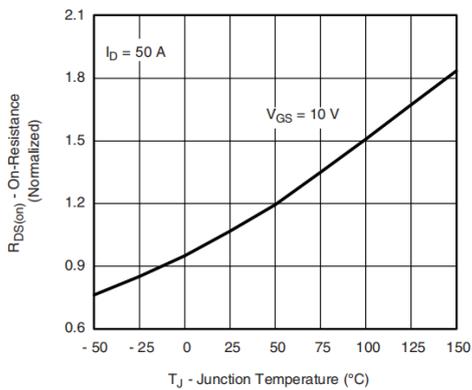
Transfer Characteristics



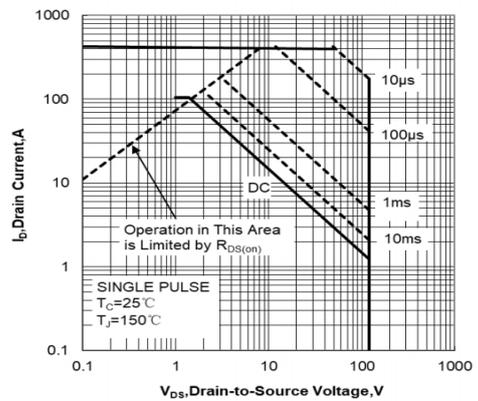
On-Resistance vs. Drain Current



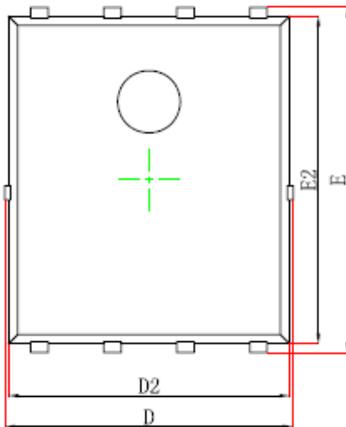
On-Resistance vs. Gate-to-Source Voltage



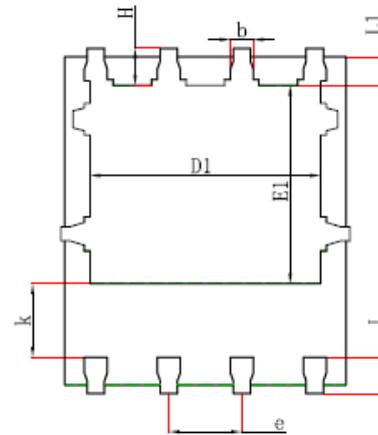
On-Resistance vs. Junction temperature



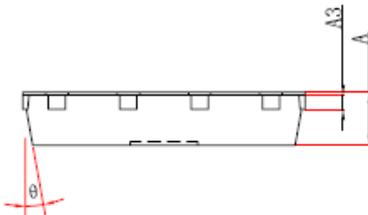
Safe Operating Area

**➤ Package Information**


Top View  
[顶视图]



Bottom View  
[背视图]



Side View  
[侧视图]

**PDFN5X6-8L**

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF		0.010REF	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP		0.050TYP	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°



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