

SSC8L60PN6

N-Channel Enhanced MOSFET

> Features

VDS	VGS	RDSON Typ.	ID
60V	±20V	1.9mR@10V	160A

Description

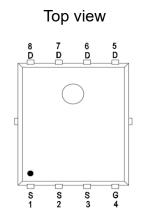
This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. 100% UIS Tested.

- > Applications
- DC/DC converters
- Power supplies
- Motor Drive Control
- Synchronous rectification

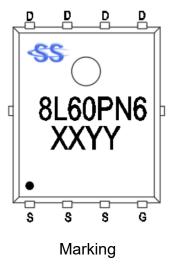
> Ordering Information

Device	Package	Shipping
SSC8L60PN6	PDFN5X6	5000/Reel

> Pin configuration







(XX: product year / YY: product week)

> Absolute Maximum Ratings(T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V _{DSS}	Drain-to-Source Vol	tage	60	V
V _{GSS}	Gate-to-Source Voltage		±20	V
	Questioners Durin Querentd	Tc=25℃	160	•
lo	Continuous Drain Current ^d	Tc=100℃	85	A
	Continuous Drain Current ^a	T _A =25℃	43	•
IDSM		T _A =70℃	31	A
I _{DM}	Pulsed Drain Curre	ent ^b	640	А
5	Power Dissipation ^c	Tc=25℃	83	14/
PD		Tc=100℃	33	W
5		T _A =25℃	5.9	14/
Pdsm	Power Dissipation ^a	T _A =70°C	3.8	W
las	Avalanche Current ^b L=0.5ml	50	А	
Eas	Avalanche Energy ^b L=0.5m	625	mJ	
TJ	Operation junction temp	-55~150		
Tstg	Storage temperature	-55~150	°C	

> Thermal Resistance Ratings($T_A=25^{\circ}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
R _{θJA}	Junction-to-Ambient Thermal Resistance ^a	21	°C/W
R _{θJC}	Junction-to-Case Thermal Resistance	1.5	C/ VV

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25°C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t ≤ 10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.



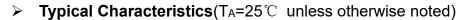
> Electronics Characteristics(TA=25°C unless otherwise noted)

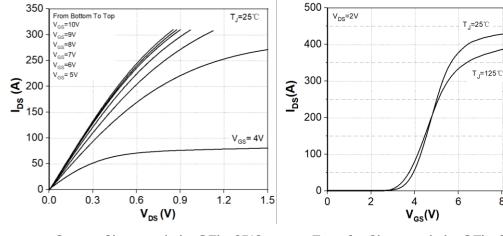
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	VGS=0V ,ID=250uA	60			V
$V_{GS \ (th)}$	Gate Threshold Voltage	VDS=VGS ,ID=250uA	1	2	3	V
$R_{DS(on)}$	Drain-Source On- Resistance	VGS=10V , ID=30A		1.9	2.6	mR
I _{DSS}	Zero Gate Voltage Drain Current	VDS=48V ,VGS=0V			1	uA
I _{GSS}	I _{GSS} Gate-Source leak current VGS=±20V ,VDS=0V				±100	nA
G _{FS}	Transconductance	VDS=5V ,ID=20A		42		S
V_{SD}	Forward Voltage	VGS=0V , IS=20A		0.8	1.3	V
Rg	Gate Resistance	VDS=0V, f=1MHz		1		R
Ciss	Input Capacitance			5029		
Coss	Output Capacitance	VDS=30V , VGS=0V,		825		pF
Crss	Reverse Transfer Capacitance	f=1MHz		56		р
T _{D(ON)}	Turn-on delay time			19		
Tr	Rise time	VGS=10V, RL=2.5R		31		
T _{D(OFF)}	Turn-off delay time	VDS=30V , RG=3R		63		ns
Tf	Fall time			23		
Q _G	Total Gate Charge	VGS=10V, VDS=30V		85		
Q _{GS}	Gate Source Charge			19		nC
Q _{GD}	Gate Drain Charge	- ID=20A		15		
Trr	Trr Diode Recovery Time IF=20A , di/dt=500			34		ns
Qrr	Diode Recovery Charge	IF=20A , di/dt=500A/us		70		nC



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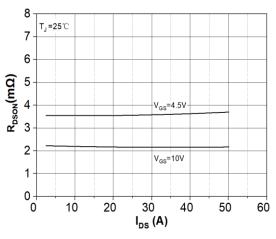
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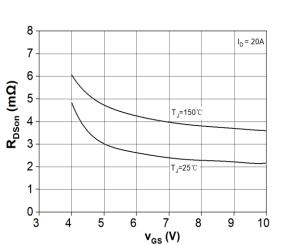


Output Characteristics@Tj= 25℃

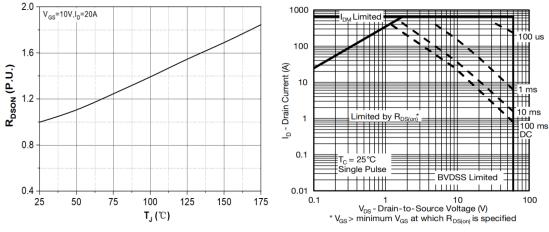




On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage

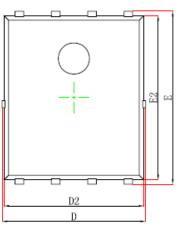


On-Resistance vs. Junction temperature

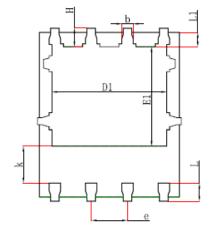
Safe Operating Area



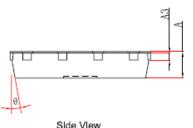
> Package Information







<u>Bottom Vlew</u> [背视图]



<u>Slde Vlew</u> [側视图]

Package: PDNF5X6-8L

Symbol	Dimensions	In Millimeters	Dimension	is In Inches	
	Min.	Max.	Min.	Max.	
А	0.900	1.000	0.035	0.039	
A3	0.254	0.254REF		OREF	
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	3.910	4.110	0.154	0.162	
E1	3.375	3.575	0.133	0.141	
D2	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.27	1.270TYP		0.050TYP	
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	



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