

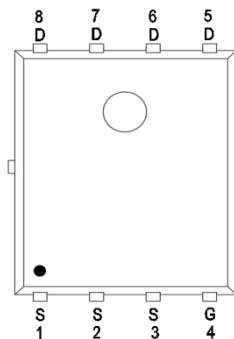
## **SSC8L36PN6**

### **N-Channel Enhanced MOSFET**

#### ➤ Features

VDS	VGS	RDS(on) Typ.	ID
30V	$\pm 20V$	1.4m $\Omega$ @10V	138A
		2.8m $\Omega$ @4V5	

#### ➤ Pin configuration



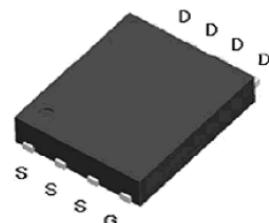
Top View

#### ➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDS(on) with low gate charge.

This device is suitable for use in DC-DC conversion, power switch and charging circuit.

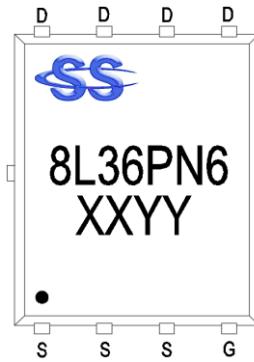
**100% UIS +  $\Delta V_{ds}$  +  $R_g$  Tested!**



PDFN5X6

#### ➤ Applications

- Load Switch
- Portable Devices
- DCDC conversion
- Power supplies
- Motor Drive Control



Marking

(XX: product year / YY: product week)

#### ➤ Ordering Information

Device	Package	Shipping
SSC8L36PN6	PDFN5X6-8L	5000/Reel

➤ **Absolute Maximum Ratings( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain-to-Source Voltage	30	V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>d</sup>	$T_c=25^\circ\text{C}$	138
		$T_c=100^\circ\text{C}$	86
$I_{DSM}$	Continuous Drain Current <sup>a</sup>	$T_A=25^\circ\text{C}$	55
		$T_A=70^\circ\text{C}$	40
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	552	A
$P_D$	Power Dissipation <sup>c</sup>	$T_c=25^\circ\text{C}$	83
		$T_c=100^\circ\text{C}$	33
$P_{DSM}$	Power Dissipation <sup>a</sup>	$T_A=25^\circ\text{C}$	7.3
		$T_A=70^\circ\text{C}$	4.5
$I_{AS}$	Avalanche Current <sup>b</sup> $L=0.5\text{mH}$ Single Pulse	38	A
$E_{AS}$	Avalanche Energy <sup>b</sup> $L=0.5\text{mH}$ Single Pulse	361	mJ
$T_J$	Operation junction temperature	-55~150	$^\circ\text{C}$
$T_{STG}$	Storage temperature range	-55~150	

➤ **Thermal Resistance Ratings( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	17	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	1.5	

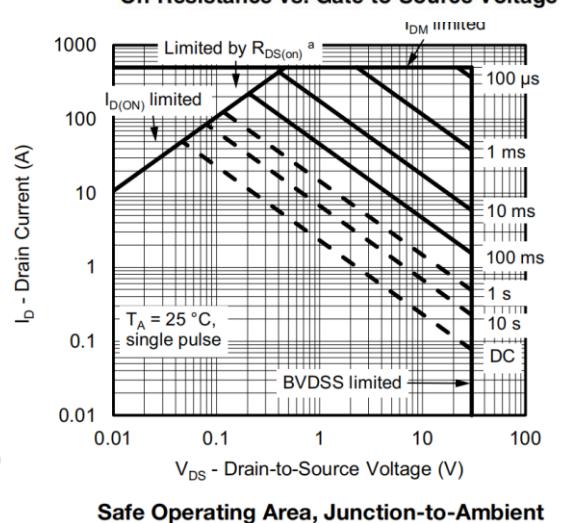
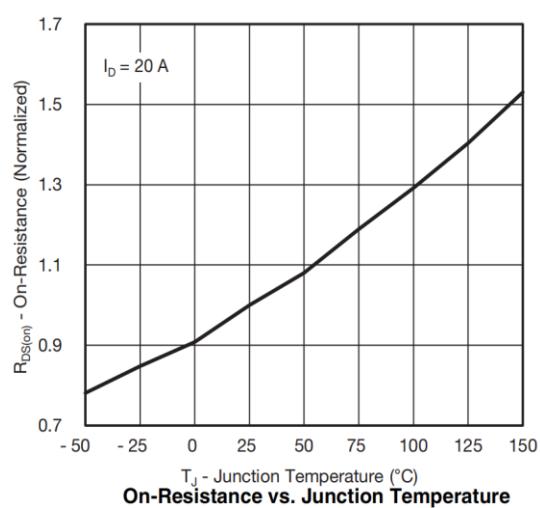
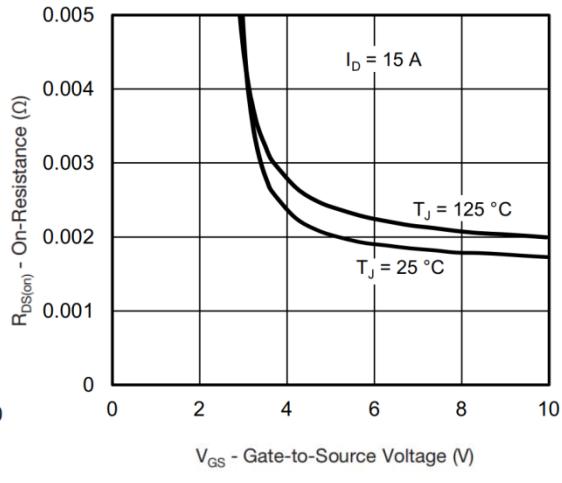
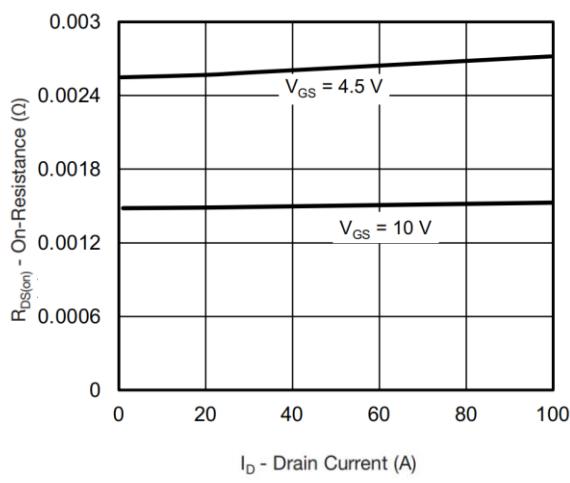
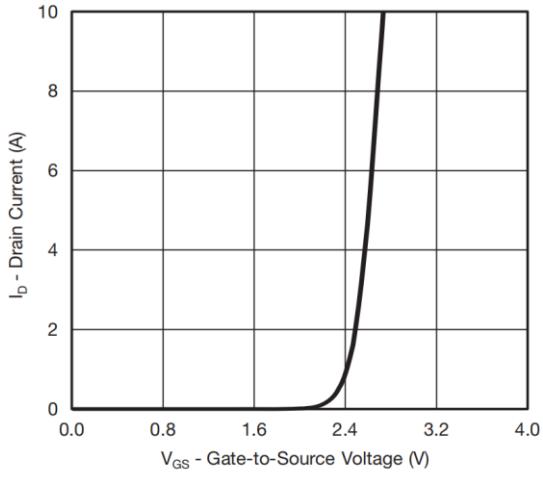
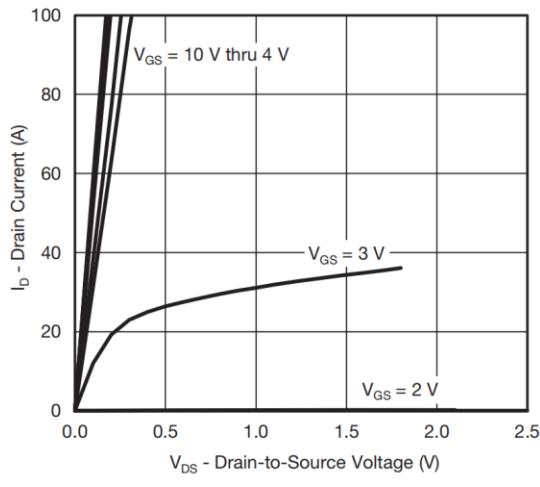
Note:

- a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^\circ\text{C}$ .The value in any given application depends on the user specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=150^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

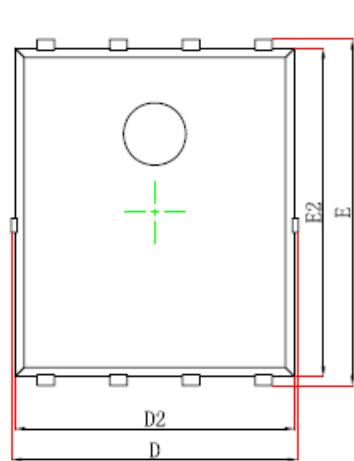
➤ Electronics Characteristics( $T_A=25^\circ C$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$VGS=0V$ , $ID=250\mu A$	30			V
$V_{GS(th)}$	Gate Threshold Voltage	$VDS=VGS$ , $ID=250\mu A$	1.1	2	2.5	V
$R_{DS(on)}$	Drain-Source On-Resistance	$VGS=10V$ , $ID=30A$		1.4	1.7	$m\Omega$
		$VGS=4.5V$ , $ID=20A$		2.8	3.8	
$I_{DSS}$	Zero Gate Voltage Drain Current	$VDS=30V$ , $VGS=0V$			1	$\mu A$
$I_{GSS}$	Gate-Source leak current	$VGS=\pm 20V$ , $VDS=0V$			$\pm 100$	nA
$G_{FS}$	Transconductance	$VDS=5V$ , $ID=20A$		26		S
$V_{SD}$	Forward Voltage	$VGS=0V$ , $IS=20A$		0.8	1.3	V
$R_g$	Gate Resistance	$VDS=0V$ , $f=1MHz$		2.8		$\Omega$
$C_{iss}$	Input Capacitance	$VDS=15V$ , $VGS=0V$ , $f=1MHz$		3378		$pF$
$C_{oss}$	Output Capacitance			1996		
$C_{rss}$	Reverse Capacitance			98		
$T_{D(ON)}$	Turn-on delay time	$VGS=10V$ , $RL=0.75\Omega$		8		$ns$
$Tr$	Rise time			6		
$T_{D(OFF)}$	Turn-off delay time			34		
$Tf$	Fall time			10		
$Q_G$	Total Gate Charge	$VGS=10V$ , $VDS=15V$ $ID=20A$		55		$nC$
$Q_{GS}$	Gate Source Charge			8		
$Q_{GD}$	Gate Drain Charge			11		
$T_{rr}$	Diode Recovery Time	$IF=20A$ , $di/dt=500A/us$		25		$ns$
$Q_{rr}$	Diode Recovery Charge	$IF=20A$ , $di/dt=500A/us$		60		$nC$

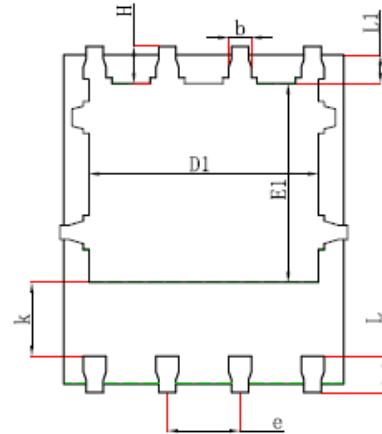
➤ **Typical Characteristics**( $T_A=25^\circ\text{C}$  unless otherwise noted)



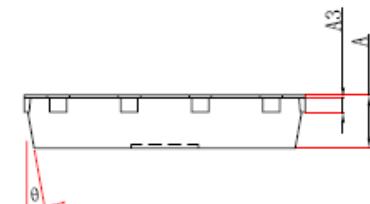
➤ Package Information



Top View  
[顶视图]



Bottom View  
[底视图]

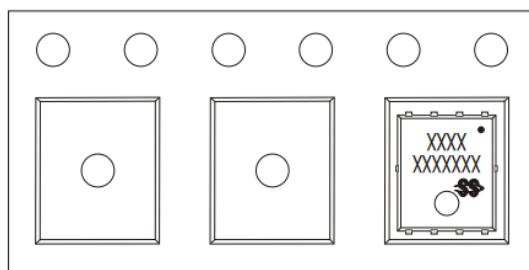
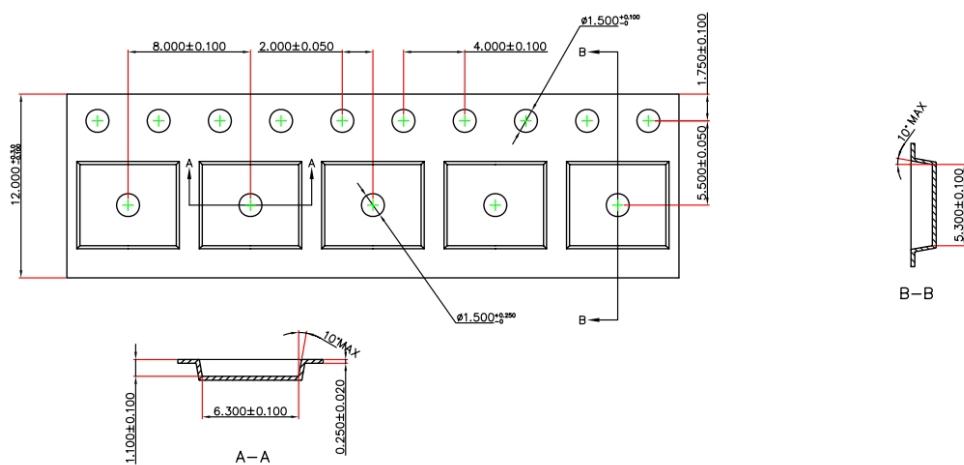
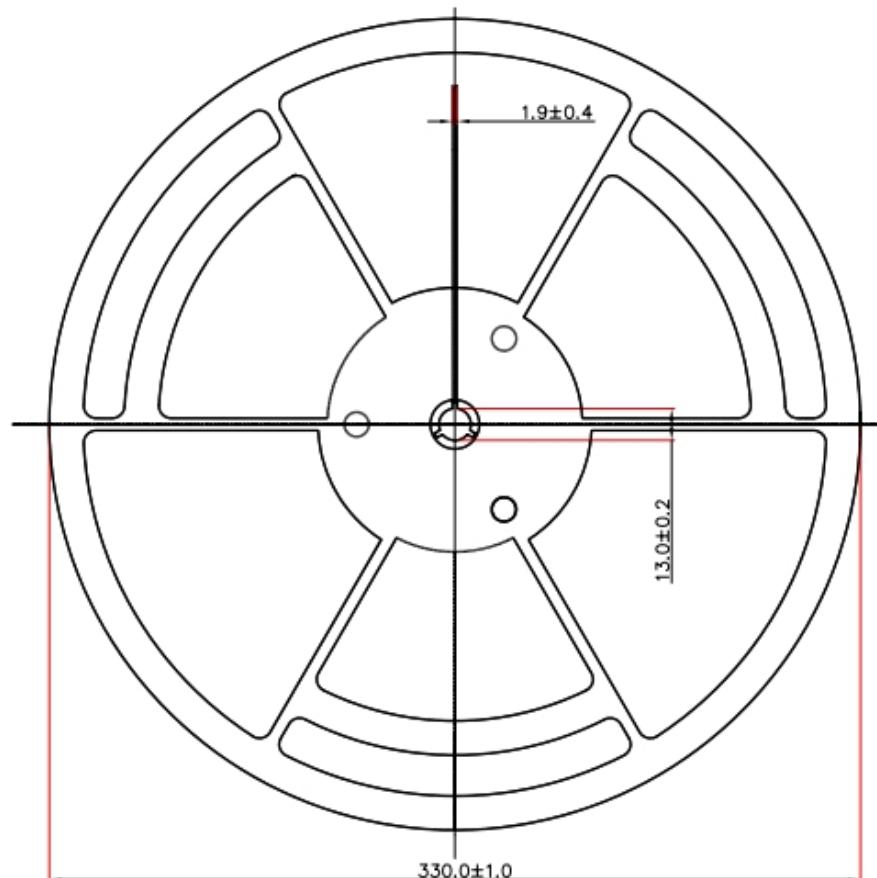


Side View  
[侧视图]

Package: PDNF5X6-8L

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF		0.010REF	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP		0.050TYP	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	10°	12°	10°	12°

## ➤ Tape and Reel





---

**DISCLAIMER**

SSCSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. SSCSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.

OUR PRODUCT SPECIFICATIONS ARE ONLY VALID IF OBTAINED THROUGH THE COMPANY'S OFFICIAL WEBSITE, CRM SYSTEM, OR OUR SALES PERSONNEL CHANNELS. IF CHANGES OR SPECIAL VERSIONS ARE INVOLVED, THEY MUST BE STAMPED WITH A QUALITY SEAL AND MARKED WITH A SPECIAL VERSION NUMBER TO BE VALID.