

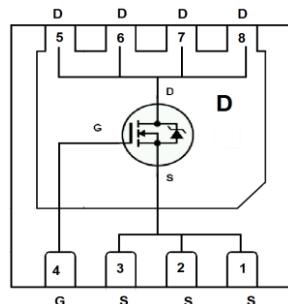
SSC8038GQ4

N-Channel Enhanced MOSFET

➤ Features

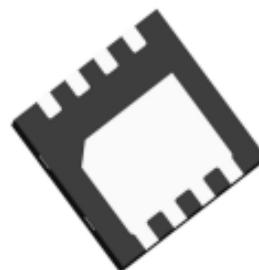
VDS	VGS	RDS(on) Typ.	ID
30V	±20V	9.5mR@10V	26A
		12.5mR@4V5	

➤ Pin configuration



➤ Description

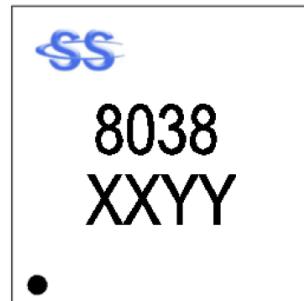
This device is N-Channel enhancement MOSFET. Uses advanced trench technology and design to provide excellent RDS(on) with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.



Bottom View

➤ Applications

- DC/DC converters
- Power supplies
- Motor Drive Control
- Synchronous rectification



Marking

(XX: product year / YY: product week)

➤ Ordering Information

Device	Package	Shipping
SSC8038GQ4	DFN3X3	5000/Reel

➤ **Absolute Maximum Ratings($T_A=25^\circ\text{C}$ unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	30	V
V_{GSS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current ^d	$T_C=25^\circ\text{C}$	26
		$T_C=100^\circ\text{C}$	19
I_{DSM}	Continuous Drain Current ^a	$T_A=25^\circ\text{C}$	18
		$T_A=70^\circ\text{C}$	12
I_{DM}	Pulsed Drain Current ^b	80	A
P_D	Power Dissipation ^c	$T_C=25^\circ\text{C}$	15.6
		$T_C=100^\circ\text{C}$	6.25
P_{DSM}	Power Dissipation ^a	$T_A=25^\circ\text{C}$	3.12
		$T_A=70^\circ\text{C}$	2
I_{AS}	Avalanche Current ^b L=0.5mH Single Pulse	14	A
E_{AS}	Avalanche Energy ^b L=0.5mH Single Pulse	49	mJ
T_J	Operation junction temperature	-55~150	$^\circ\text{C}$
T_{STG}	Storage temperature range	-55~150	

➤ **Thermal Resistance Ratings($T_A=25^\circ\text{C}$ unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	40	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	8	

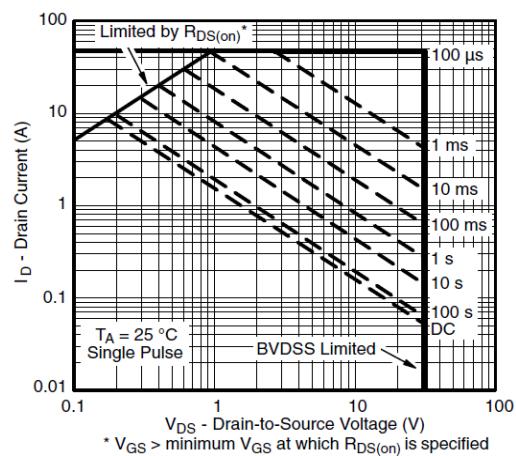
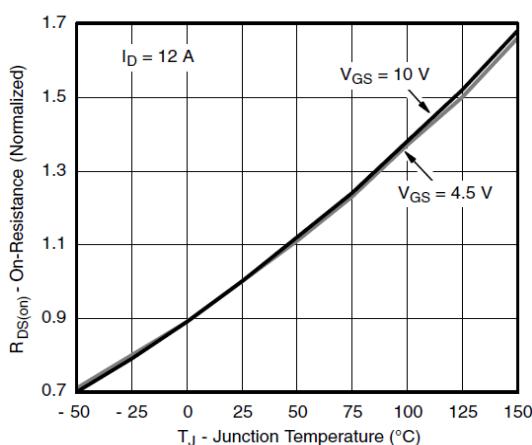
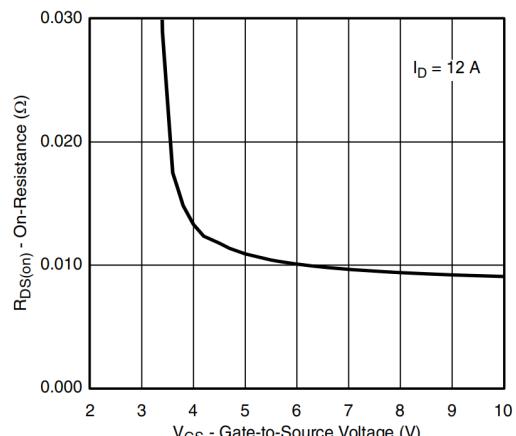
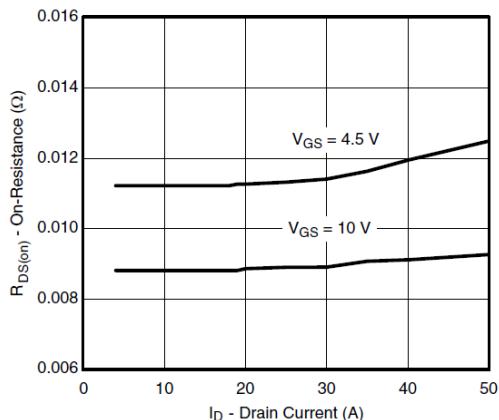
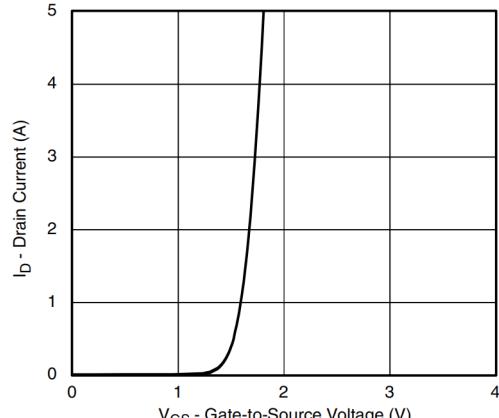
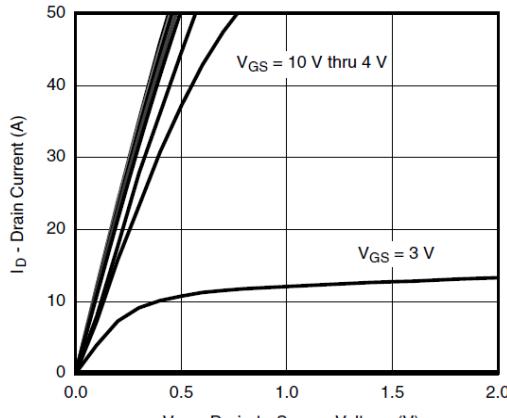
Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user specific board design. The power dissipation is based on the t $\leq 10\text{s}$ thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

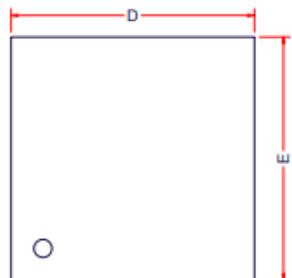
➤ Electronics Characteristics($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, ID=250\mu A$	30			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, ID=250\mu A$	1.1	1.3	2.2	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V, ID=15A$		9.5	12	mR
		$V_{GS}=4.5V, ID=10A$		12.5	16	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$			1	μA
I_{GSS}	Gate-Source leak current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
G_{FS}	Transconductance	$V_{DS}=15V, ID=12A$		32		S
V_{SD}	Forward Voltage	$V_{GS}=0V, IS=5A$		0.8	1.3	V
R_g	Gate Resistance	$V_{DS}=0V, f=1MHz$		4		R
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1MHz$		900		pF
C_{oss}	Output Capacitance			175		
C_{rss}	Reverse Transfer Capacitance			160		
$T_{D(ON)}$	Turn-on delay time	$V_{GS}=10V, RL=10R$ $V_{DS}=10V, RG=1R$		10		ns
T_r	Rise time			10		
$T_{D(OFF)}$	Turn-off delay time			32		
T_f	Fall time			8		
Q_G	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V$ $ID=15A$		28		nC
Q_{GS}	Gate Source Charge			3.9		
Q_{GD}	Gate Drain Charge			6.4		
T_{rr}	Diode Recovery Time	$IF=10A, di/dt=100A/\mu s$		12		ns
Q_{rr}	Diode Recovery Charge	$IF=10A, di/dt=100A/\mu s$		15		nC

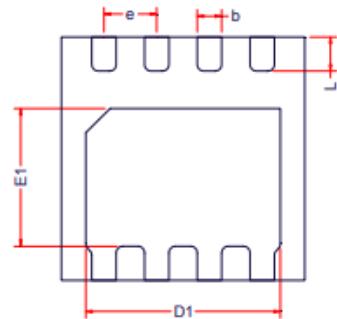
➤ **Typical Characteristics**($T_A=25^\circ\text{C}$ unless otherwise noted)



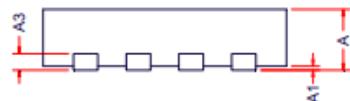
➤ Package Information



TOP VIEW



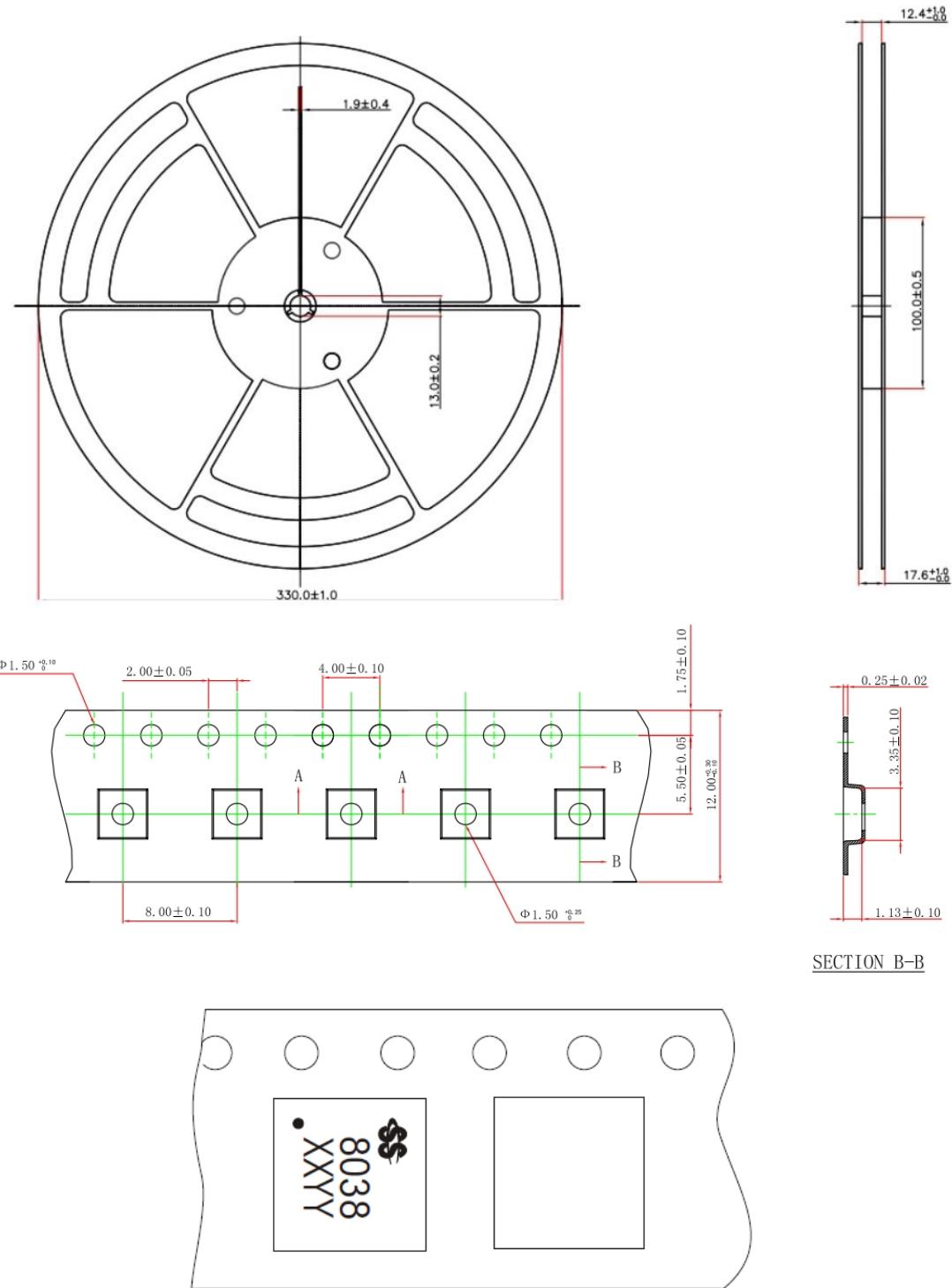
BOTTOM VIEW



SIDE VIEW

Package: DNF3X3-8L

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.20Ref		
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D1	2.35	2.40	2.45
E1	1.65	1.70	1.75
b	0.25	0.30	0.35
e	0.65BSC		
L	0.37	0.42	0.47

➤ **Tape and Reel**




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