



## SSC138GS7

### N-Channel Small Switching MOSFET with ESD Protection

#### Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>
50V	±20V	0.6Ω@5V0	0.2A
		0.9Ω@2V75	

#### Description

This device is an N-Channel enhancement mode MOSFET, with low on-resistance, fast switching speed and low threshold voltage, it is ideal for portable equipment.

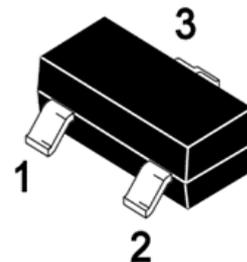
#### Applications

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers
- Display, Memories, Transistors, etc.
- Battery Operated System
- Solid-State Relays

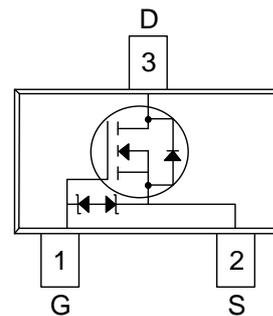
#### Ordering Information

Device	Package	Shipping
SSC138GS7	SOT-323	3000/Reel

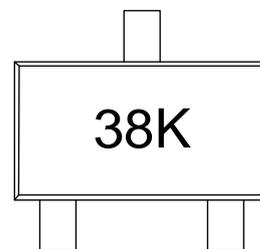
#### Pin configuration



**SOT-323**



**Pin Configuration (Top View)**



**Marking**



➤ **Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain-to-Source Voltage	50	V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>a</sup>	0.2	A
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	0.8	A
$P_D$	Power Dissipation <sup>c</sup>	0.3	W
$T_J$	Operation junction temperature	-55~150	$^{\circ}\text{C}$
$T_{STG}$	Storage temperature range	-55~150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	370	$^{\circ}\text{C}/\text{W}$

Note:

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The value in any given application depends on the user is specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_D$  is based on  $T_{J(\text{MAX})}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

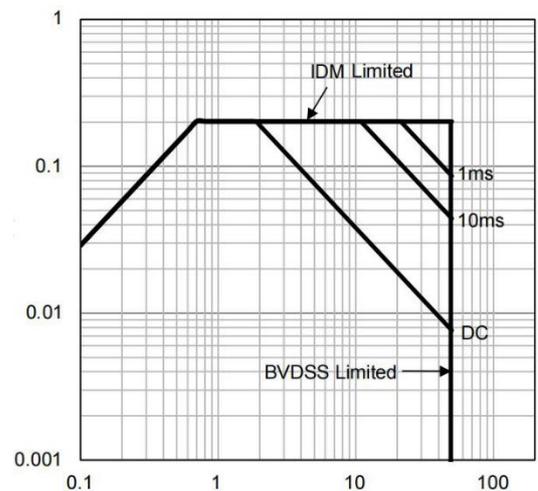
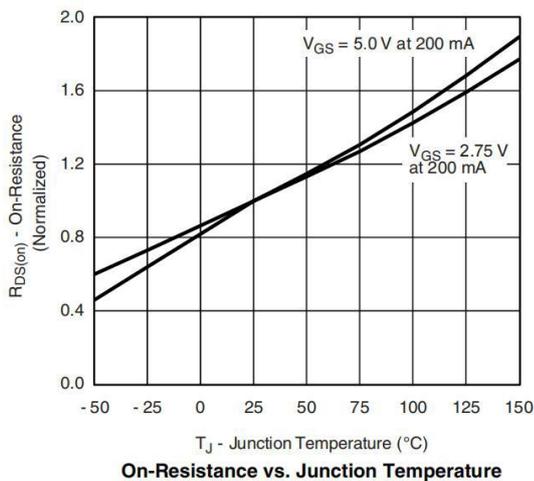
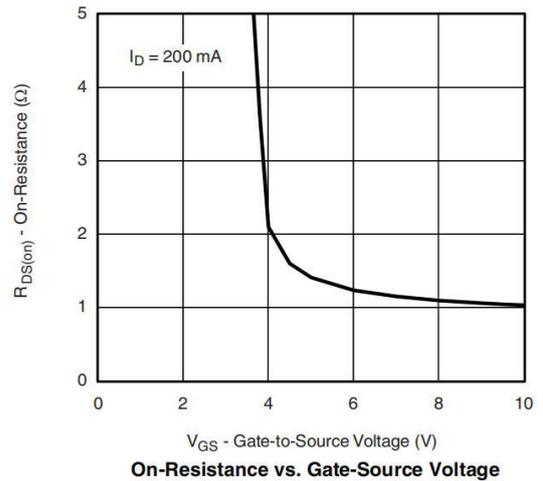
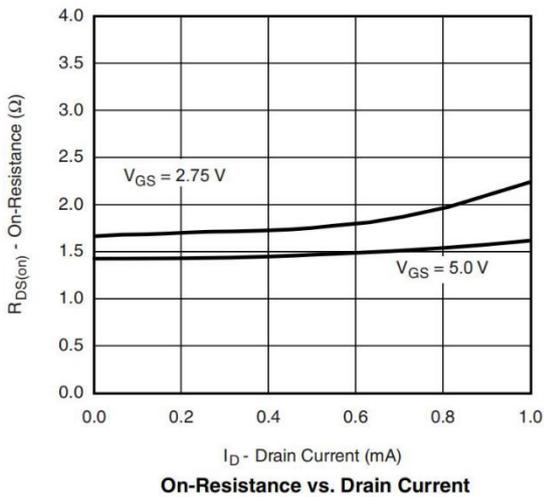
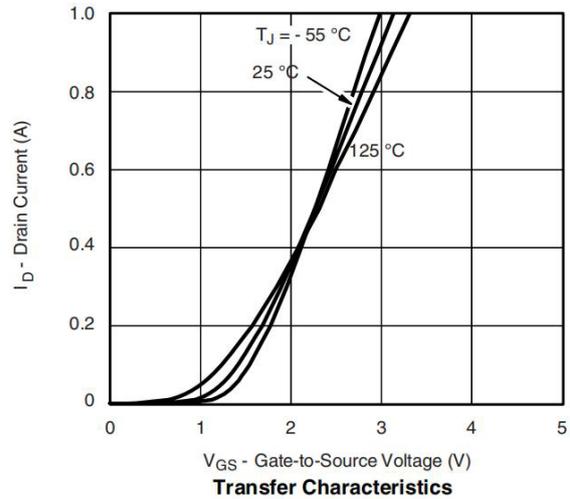
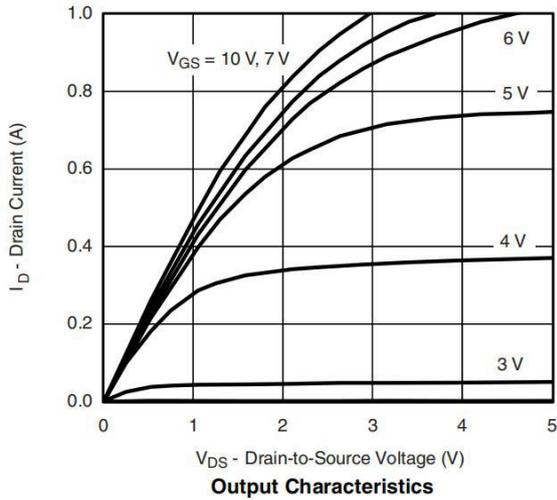


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

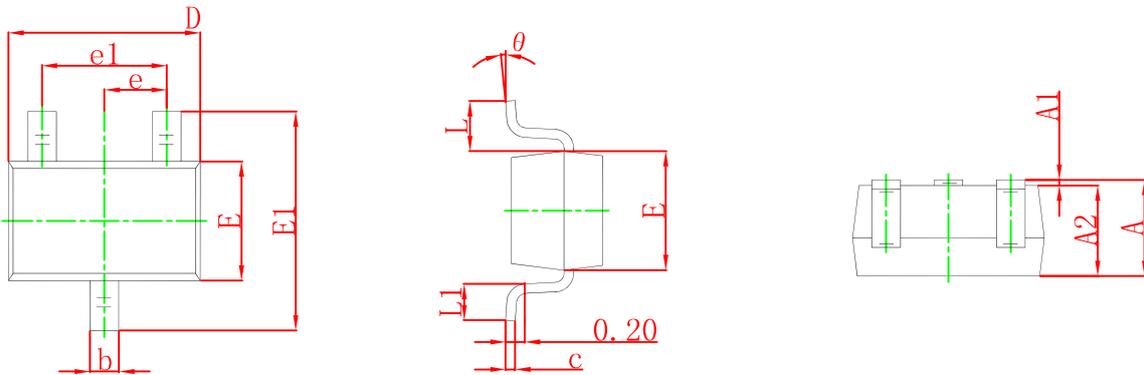
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	50			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	0.5		1.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 5V, I <sub>D</sub> = 0.2A		0.6	4	Ω
		V <sub>GS</sub> = 2.75V, I <sub>D</sub> = 0.2A		0.9	10	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±10	μA
Transconductance	G <sub>FS</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A	0.1			s
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.2A			1.3	V
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		27		pF
Output Capacitance	C <sub>OSS</sub>			13		
Reverse Transfer Capacitance	C <sub>RSS</sub>			6		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, R <sub>L</sub> = 6Ω V <sub>DS</sub> = 30V, I <sub>D</sub> = 0.2A			5	ns
Rise Time	T <sub>r</sub>				18	
Turn-off Delay Time	T <sub>D(OFF)</sub>				36	
Fall Time	T <sub>f</sub>				14	
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 25V, I <sub>D</sub> = 0.2A		0.45		nC
Gate to Source Charge	Q <sub>GS</sub>			0.95		
Gate to Drain Charge	Q <sub>GD</sub>			0.10		



➤ **Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

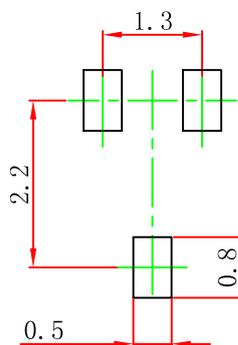


## ➤ Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

## ➤ Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.



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