



## SSC8L3316GN4

### Dual N-Channel Enhancement MOSFET

#### Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>
30V	±20V	5.8mΩ@10V	47A
		7.4mΩ@4.5V	

#### Description

The device is N-Channel enhancement mode MOSFET. Uses SGT Technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC - DC conversion, power switch and charging circuit.

**100% UIS + ΔVDS + Rg Tested!**

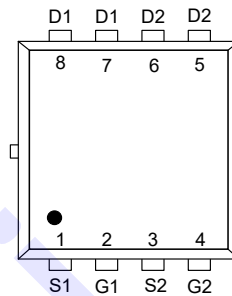
#### Applications

- Inverter
- DC-DC Converter
- Half and Full Bridge Topology
- Motor Drive Control

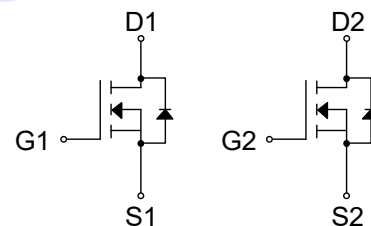
#### Ordering Information

Device	Package	Shipping
SSC8L3316GN4	PDFN3.3X3.3-8L	5000/Reel

#### Pin configuration



**PDFN3.3x3.3-8L (Top View)**



**Pin Configuration**



**Marking**

(XXYY: Internal Traceability Code)



### ➤ Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
$V_{DSS}$	Drain-to-Source Voltage	30	V	
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V	
$I_D$	Continuous Drain Current <sup>b</sup>	$T_C = 25^\circ\text{C}$	47	A
		$T_C = 100^\circ\text{C}$	30	A
$I_{DSM}$	Continuous Drain Current <sup>a</sup>	$T_A = 25^\circ\text{C}$	14	A
		$T_A = 70^\circ\text{C}$	11	A
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	188	A	
$P_D$	Power Dissipation <sup>c</sup>	$T_C = 25^\circ\text{C}$	28	W
		$T_C = 100^\circ\text{C}$	11	W
$P_{DSM}$	Power Dissipation <sup>a</sup>	$T_A = 25^\circ\text{C}$	2.5	W
		$T_A = 70^\circ\text{C}$	1.6	W
$I_{AS}$	Avalanche Current <sup>b</sup> $L = 0.5\text{mH}$	12	A	
$E_{AS}$	Avalanche Energy <sup>b</sup> $L = 0.5\text{mH}$	36	mJ	
$T_J$	Operation junction temperature	-55 to 150	$^\circ\text{C}$	
$T_{STG}$	Storage temperature range	-55 to 150	$^\circ\text{C}$	

### ➤ Thermal Resistance Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	50	65	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	4.5	5.9	

Note:

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The value in any given application depends on the user's specific board design. The current rating is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_D$  is based on  $T_{J(\text{MAX})}=150^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

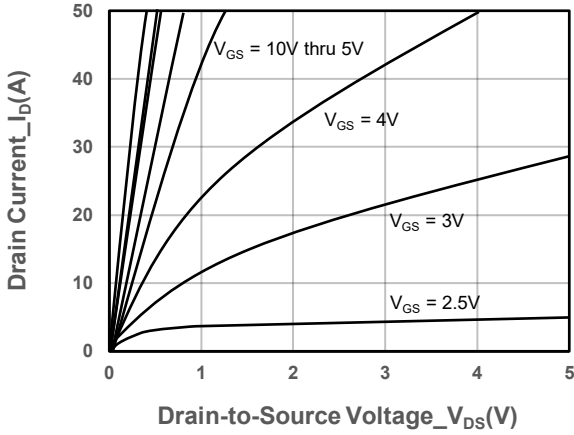


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

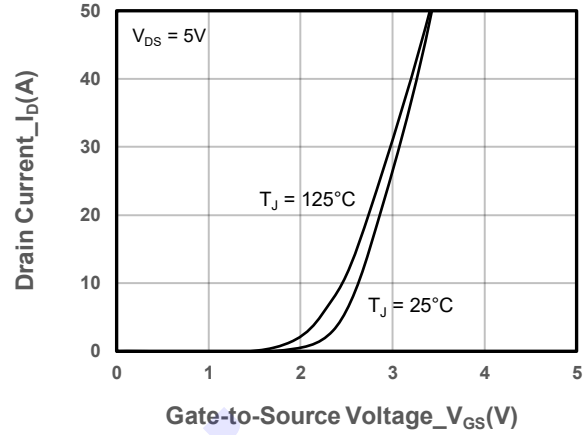
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1	1.7	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		5.8	7.5	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		7.4	9.6	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A			1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		2.5		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz		980		pF
Output Capacitance	C <sub>OSS</sub>			770		
Reverse Transfer Capacitance	C <sub>RSS</sub>			51		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 15A		19		nC
Gate to Source Charge	Q <sub>GS</sub>			4.1		
Gate to Drain Charge	Q <sub>GD</sub>			3.6		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 15A, R <sub>G</sub> = 3Ω		6.8		ns
Rise Time	T <sub>r</sub>			4		
Turn-off Delay Time	T <sub>D(OFF)</sub>			21		
Fall Time	T <sub>f</sub>			4.5		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =15A, di/dt=100A/us		30		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =15A, di/dt=100A/us		15		nC



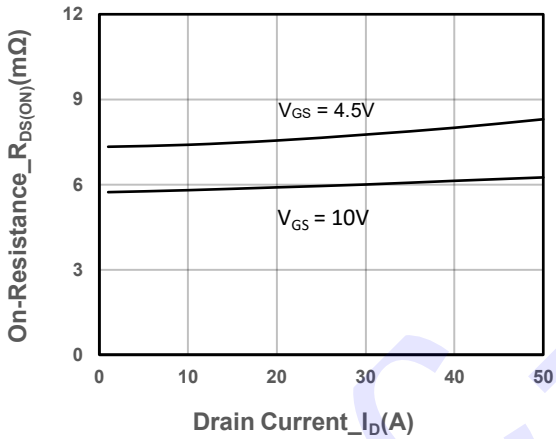
➤ **Typical Performance Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**



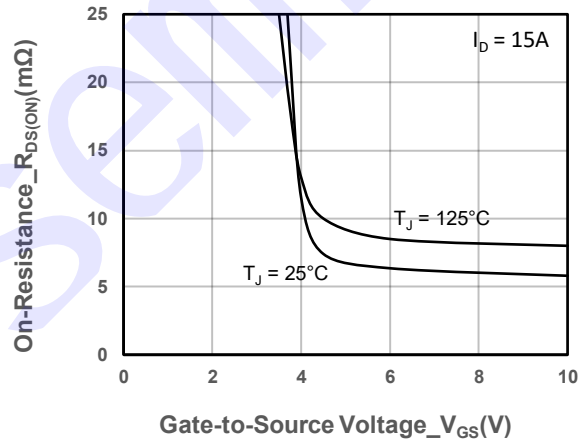
**Output Characteristics**



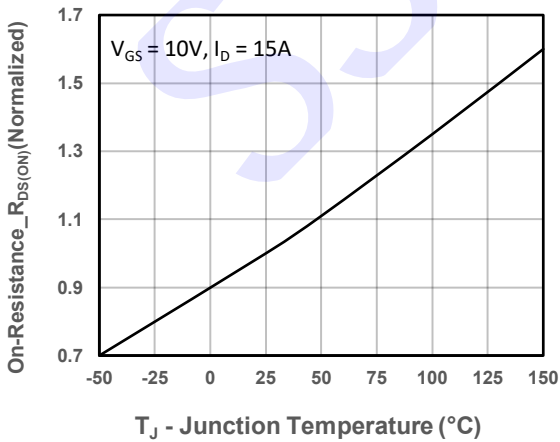
**Transfer Characteristics**



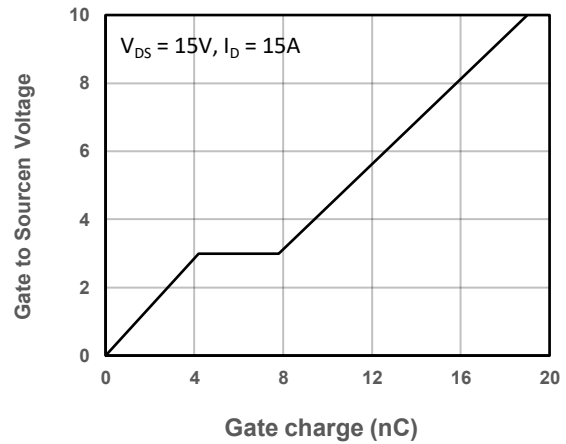
**On-Resistance vs. Drain Current and Gate Voltage**



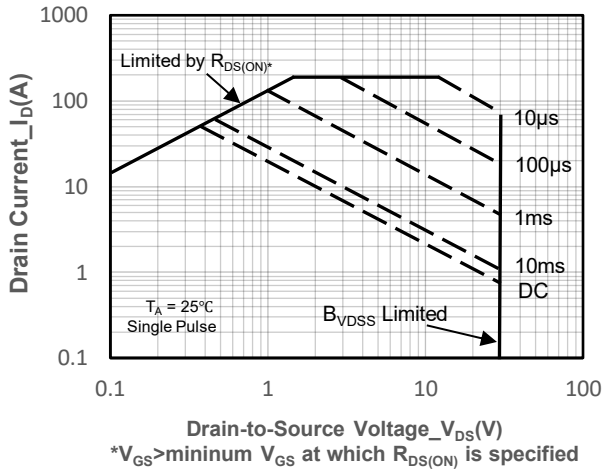
**On-Resistance vs. Gate-to-Source Voltage**



**On-Resistance vs. Junction Temperature**



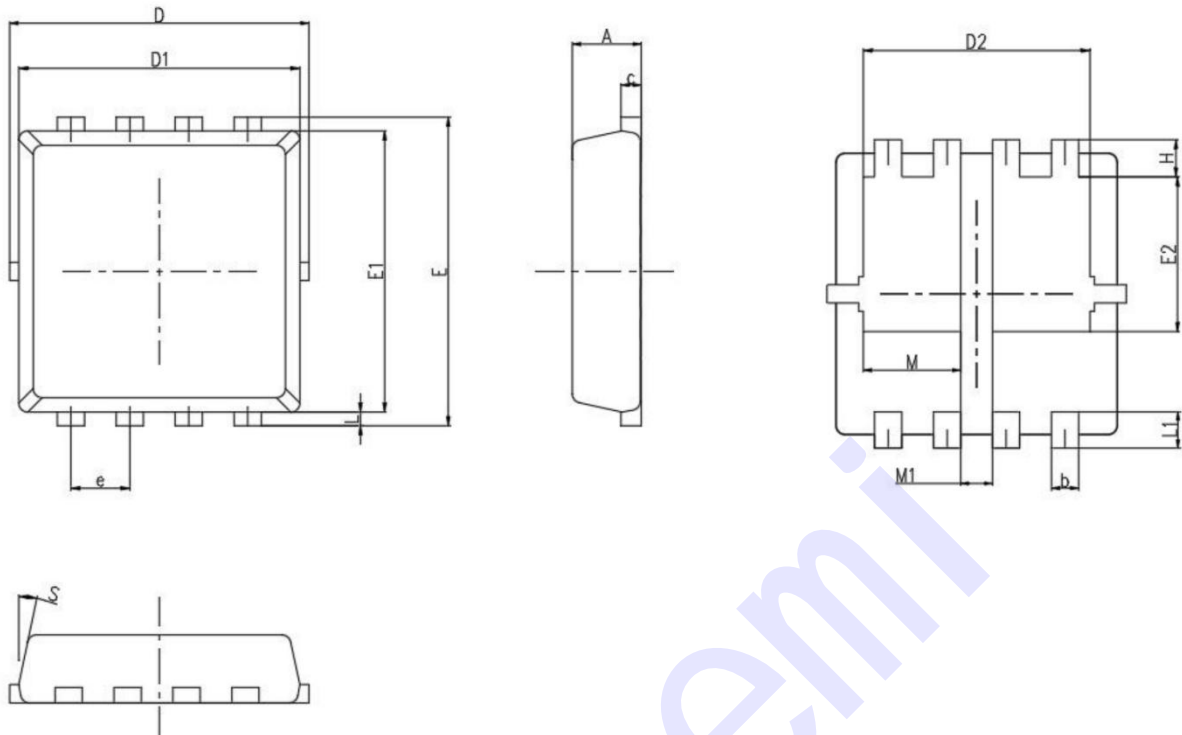
**Gate-Source Voltage vs. Gate charge**



Safe Operating Area vs. Junction-to-Ambient

SSC-semi

## ➤ Package Information



Symbol	MILL IMETER		
	Min	Nom	Max
A	0.60	0.75	0.90
b	0.25	0.30	0.35
c	0.10	0.20	0.30
D	3.00	3.20	3.45
D1	3.05	3.15	3.25
D2	2.40	2.50	2.60
E	3.10	3.30	3.50
E1	2.90	3.05	3.20
E2	1.55	1.75	1.95
e	0.65BSC		
H	0.20	0.40	0.57
L	0.06	0.10	0.20
L1	0.30	0.40	0.55
S	10°	12°	14°
M	0.95	1.05	1.15
M1	0.4BSC		



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SSC-semi