



## SSCL6N40GQ4

### N-Channel Enhancement Mode MOSFET

#### ➤ Features

$V_{DS}$	$V_{GS}$	$R_{DS(ON)}$ Typ.	$I_D$
40V	$\pm 20V$	6 m $\Omega$ @10V	53A
		7 m $\Omega$ @4.5V	

#### ➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

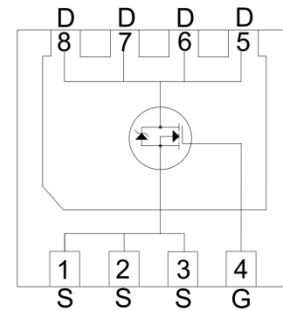
#### ➤ Applications

- Motor Drive Control
- DC/DC Conversion
- Half and Full Bridge Topology
- Inverter

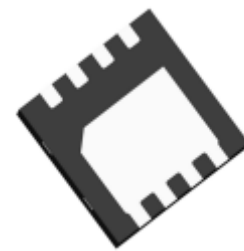
#### ➤ Ordering Information

Device	Package	Shipping
SSCL6N40GQ4	DFN3X3	5000/Reel

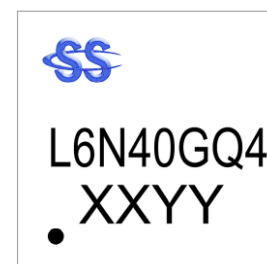
#### ➤ Pin Configuration



**DFN3X3 (Top View)**



**Bottom View**



**Marking**

(XXYY: Internal Traceability Code)

**➤ Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter		Ratings	Unit
$V_{\text{DSS}}$	Drain-to-Source Voltage		40	V
$V_{\text{GSS}}$	Gate-to-Source Voltage		$\pm 20$	V
$I_{\text{D}}$	Continuous Drain Current <sup>d</sup>	$T_{\text{C}}=25^{\circ}\text{C}$	53	A
		$T_{\text{C}}=100^{\circ}\text{C}$	33	
$I_{\text{DSM}}$	Continuous Drain Current <sup>a</sup>	$T_{\text{A}}=25^{\circ}\text{C}$	16	A
		$T_{\text{A}}=70^{\circ}\text{C}$	13	
$I_{\text{DM}}$	Pulsed Drain Current <sup>b</sup>		212	A
$P_{\text{D}}$	Power Dissipation <sup>c</sup>	$T_{\text{C}}=25^{\circ}\text{C}$	36	W
		$T_{\text{C}}=100^{\circ}\text{C}$	14	
$P_{\text{DSM}}$	Power Dissipation <sup>a</sup>	$T_{\text{A}}=25^{\circ}\text{C}$	3.2	W
		$T_{\text{A}}=70^{\circ}\text{C}$	2.1	
$I_{\text{AS}}$	Avalanche Current <sup>b</sup> $L=0.5\text{mH}$ Single Pulse		15	A
$E_{\text{AS}}$	Avalanche Energy <sup>b</sup> $L=0.5\text{mH}$ Single Pulse		56	mJ
$T_{\text{J}}$	Operation junction temperature		-55~150	$^{\circ}\text{C}$
$T_{\text{STG}}$	Storage temperature range		-55~150	

**➤ Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Max.	Unit
$R_{\theta\text{JA}}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	39	50	$^{\circ}\text{C}/\text{W}$
$R_{\theta\text{JC}}$	Junction-to-Case Thermal Resistance	3.5	4.5	

Note:

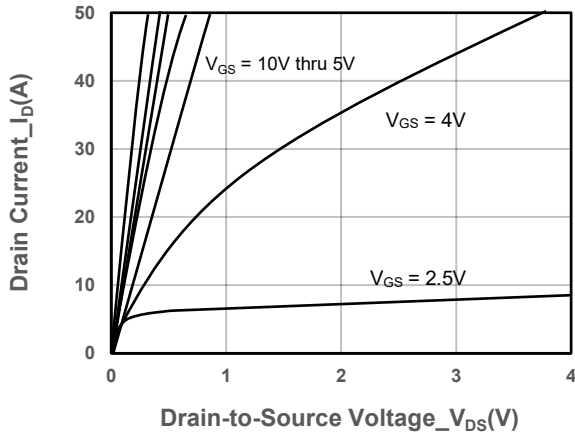
- The value of  $R_{\theta\text{JA}}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_{\text{A}}=25^{\circ}\text{C}$ . The value in any given application depends on the user is specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_{\text{D}}$  is based on  $T_{\text{J}(\text{MAX})}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

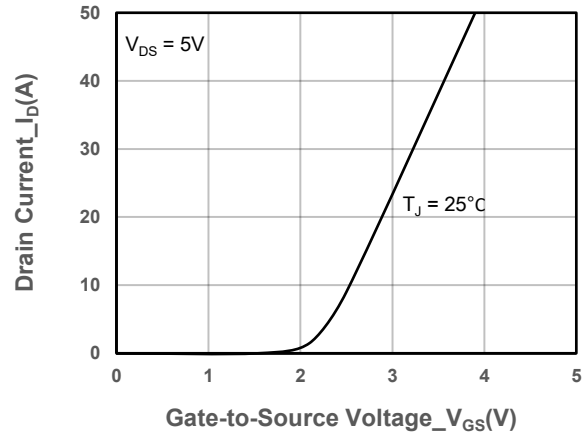
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	40			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1	1.6	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		6	8	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		7	10	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A		0.75	1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		2.0		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1MHz		958		pF
Output Capacitance	C <sub>OSS</sub>			610		
Reverse Transfer Capacitance	C <sub>RSS</sub>			30		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 20V, I <sub>D</sub> = 20A		20		nC
Gate to Source Charge	Q <sub>GS</sub>			4.2		
Gate to Drain Charge	Q <sub>GD</sub>			3.5		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 20V, I <sub>D</sub> = 20A, R <sub>G</sub> = 3Ω		7		ns
Rise Time	T <sub>r</sub>			3		
Turn-off Delay Time	T <sub>D(OFF)</sub>			25		
Fall Time	T <sub>f</sub>			5		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		38		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		24		nC



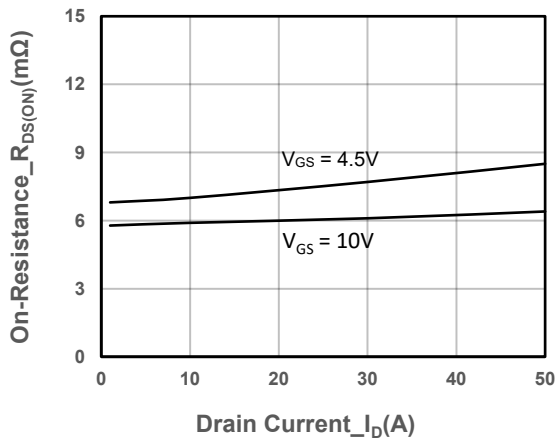
## ➤ Typical Performance Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)



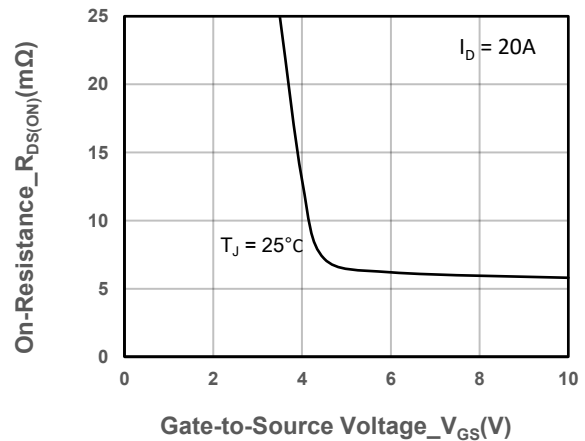
Output Characteristics



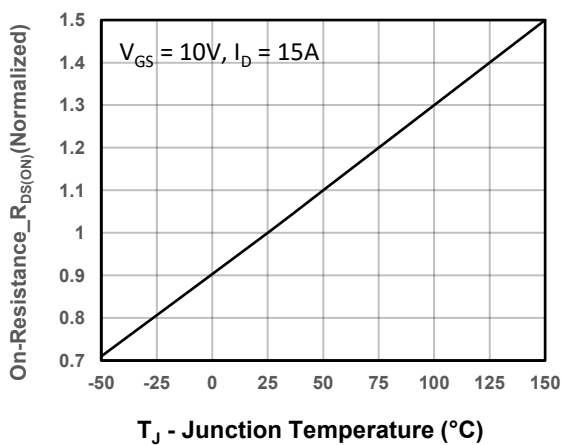
Transfer Characteristics



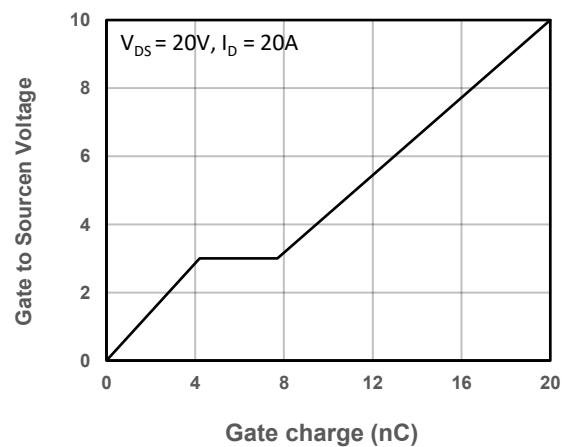
On-Resistance vs. Drain Current and Gate Voltage



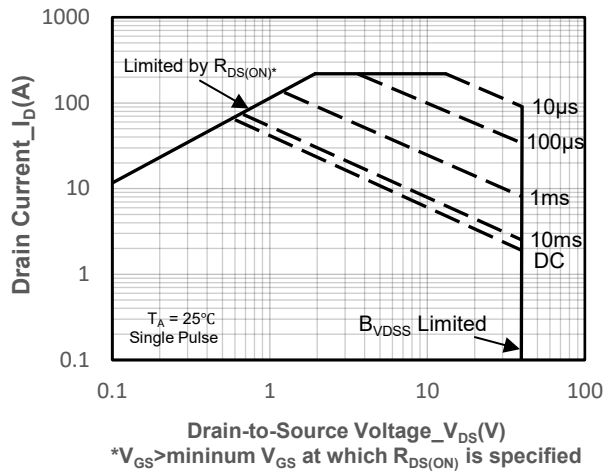
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

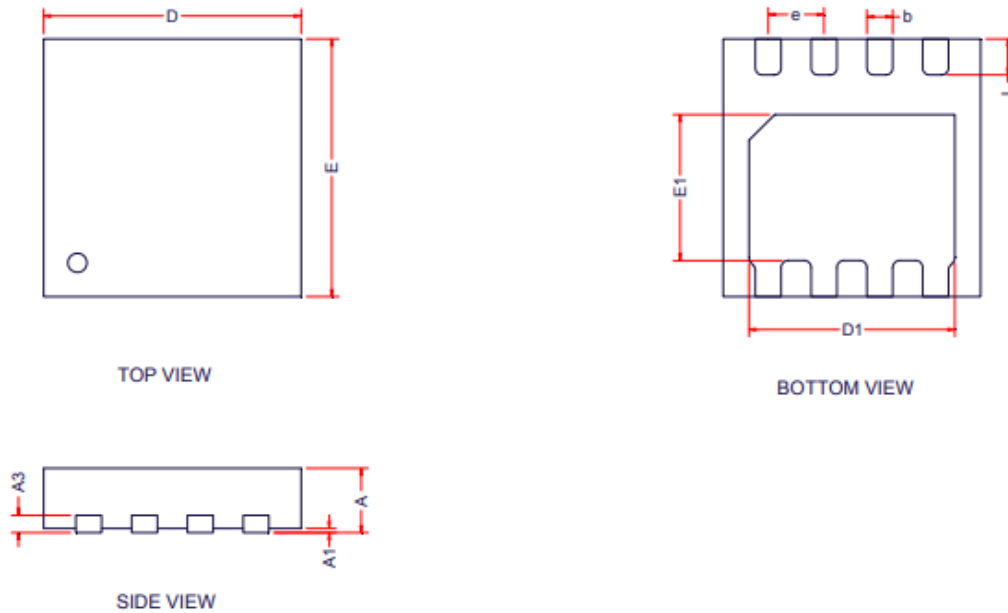


Gate-Source Voltage vs. Gate charge



**Safe Operating Area vs. Junction-to-Ambient**

## ➤ Package Information



DFN3X3

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.20Ref		
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D1	2.35	2.40	2.45
E1	1.65	1.70	1.75
b	0.25	0.30	0.35
e	0.65BSC		
L	0.37	0.42	0.47



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