

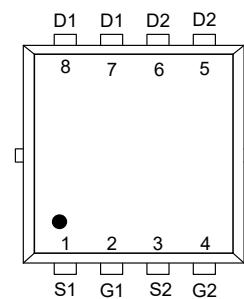
## SSC8L3622GN4

### Dual N-Channel Enhancement MOSFET

#### ➤ Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>
60V	±20V	12.5mΩ@10V	32A
		16mΩ@4.5V	

#### ➤ Pin configuration



PDFN3.3x3.3-8L (Top View)

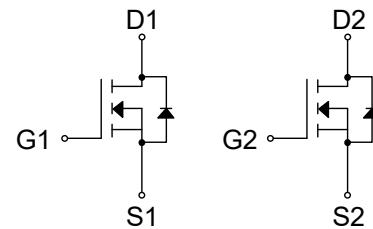
#### ➤ Description

The SSC8L3622GN4 is N-Channel enhancement mode MOSFET. Uses SGT Technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC - DC conversion, power switch and charging circuit.

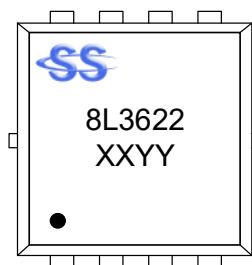
**100% UIS + ΔVDS + Rg Tested!**

#### ➤ Applications

- Inverter
- DC-DC Converter
- Half and Full Bridge Topology
- Motor Drive Control



Pin Configuration



Marking

(XXYY: Internal Traceability Code)

**➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Ratings	Unit	
V <sub>DSS</sub>	Drain-to-Source Voltage	60	V	
V <sub>GSS</sub>	Gate-to-Source Voltage	±20	V	
I <sub>D</sub>	Continuous Drain Current <sup>b</sup>	T <sub>C</sub> = 25°C	32	A
		T <sub>C</sub> = 100°C	18	A
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	120	A	
I <sub>DSM</sub>	Continuous Drain Current <sup>a</sup>	T <sub>A</sub> = 25°C	10	A
		T <sub>A</sub> = 70°C	7	A
P <sub>D</sub>	Power Dissipation <sup>c</sup>	T <sub>C</sub> = 25°C	21	W
		T <sub>C</sub> = 100°C	8.6	W
P <sub>DSM</sub>	Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25°C	2.3	W
		T <sub>A</sub> = 70°C	1.45	W
I <sub>AS</sub>	Avalanche Current <sup>b</sup> L = 0.5mH	15	A	
E <sub>AS</sub>	Avalanche Energy <sup>b</sup> L = 0.5mH	56.25	mJ	
T <sub>J</sub>	Operation junction temperature	-55 to 150	°C	
T <sub>STG</sub>	Storage temperature range	-55 to 150	°C	

**➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
R <sub>θJA</sub>	Junction-to-Ambient Thermal Resistance <sup>a</sup>	55	°C/W
R <sub>θJC</sub>	Junction-to-Case Thermal Resistance	5.8	

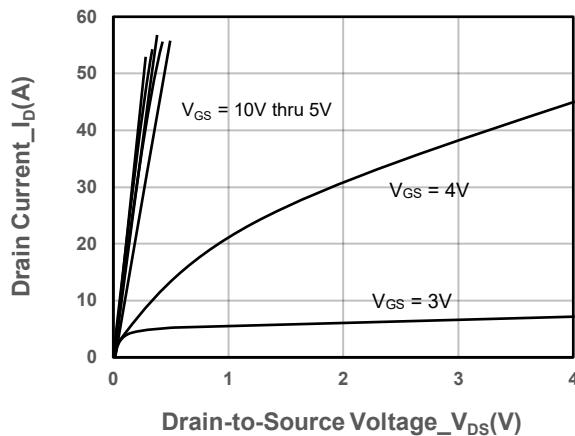
Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper,in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user specific board design. The current rating is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

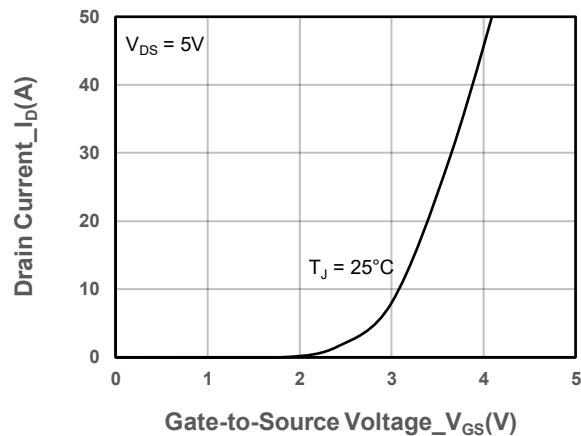
➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1	1.7	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		12.5	17	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		16	21	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A		0.7	1.2	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		1.3		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz		600		pF
Output Capacitance	C <sub>OSS</sub>			205		
Reverse Transfer Capacitance	C <sub>RSS</sub>			8		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 15A		9.6		nC
Gate to Source Charge	Q <sub>GS</sub>			1.9		
Gate to Drain Charge	Q <sub>GD</sub>			1.3		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 15A, R <sub>G</sub> = 3Ω		3.5		ns
Rise Time	T <sub>r</sub>			10.8		
Turn-off Delay Time	T <sub>D(OFF)</sub>			9.6		
Fall Time	T <sub>f</sub>			4.3		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		23		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		13.5		nC

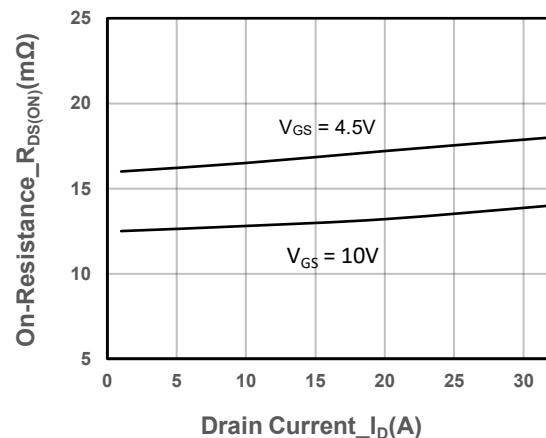
➤ **Typical Performance Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**



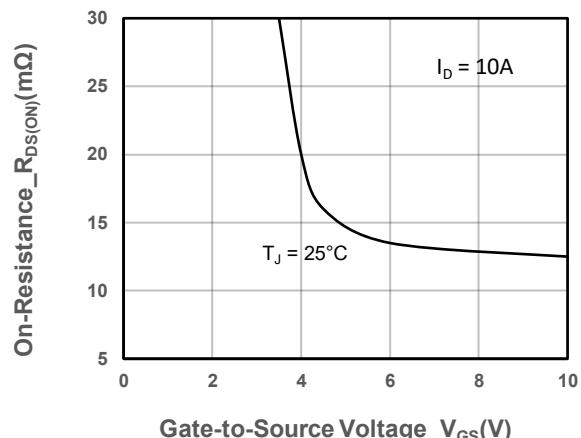
**Output Characteristics**



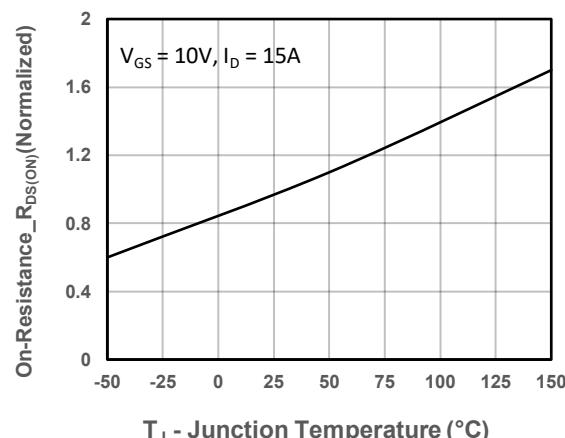
**Transfer Characteristics**



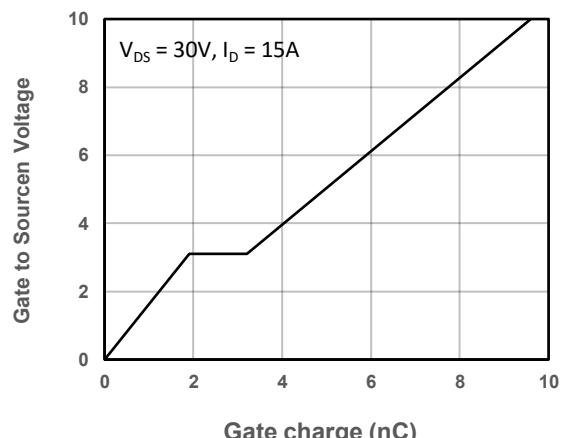
**On-Resistance vs. Drain Current and Gate Voltage**



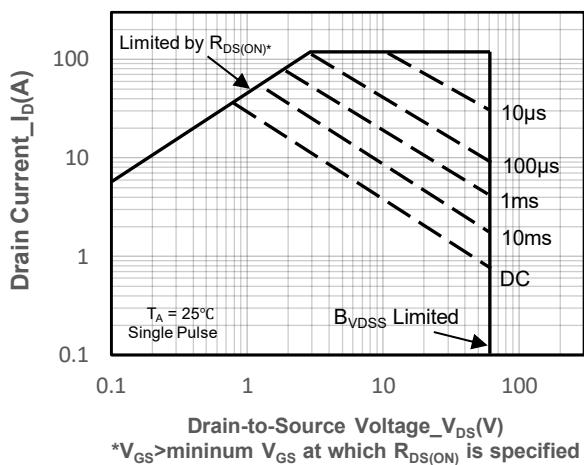
**On-Resistance vs. Gate-to-Source Voltage**



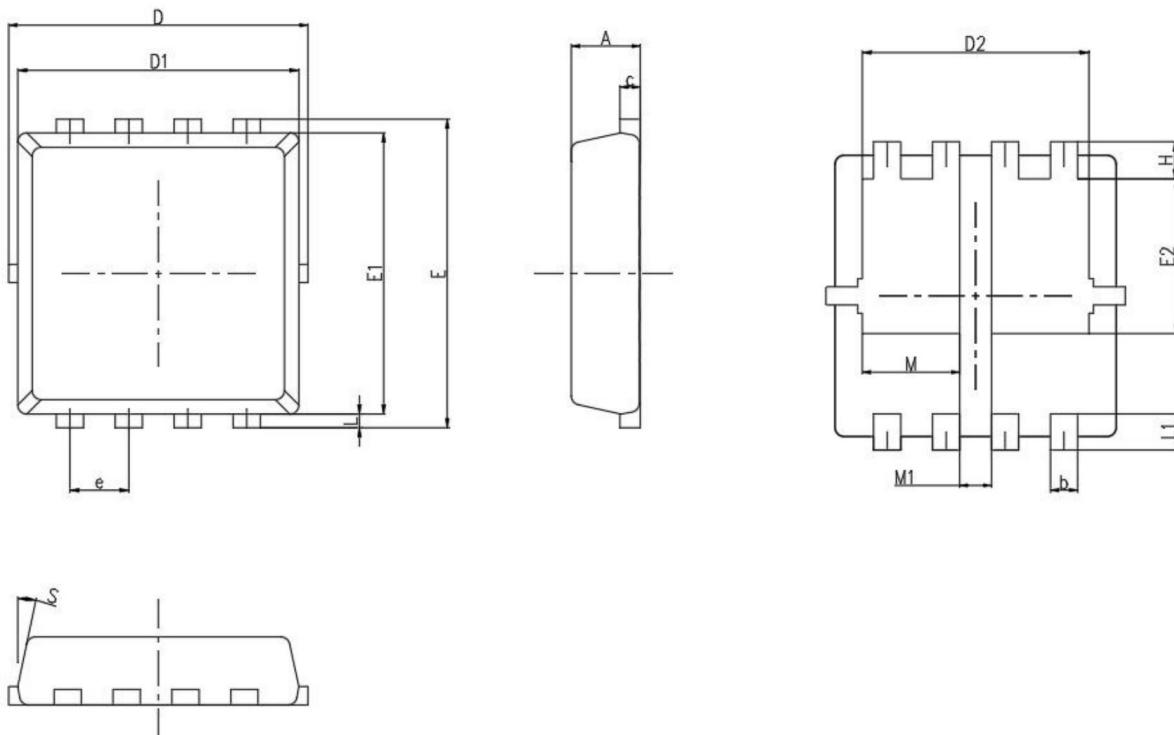
**On-Resistance vs. Junction Temperature**



**Gate-Source Voltage vs. Gate charge**

**Safe Operating Area vs. Junction-to-Ambient**

➤ **Package Information**



Symbol	MILL IMETER		
	Min	Nom	Max
A	0.60	0.75	0.90
b	0.25	0.30	0.35
c	0.10	0.20	0.30
D	3.00	3.20	3.45
D1	3.05	3.15	3.25
D2	2.40	2.50	2.60
E	3.10	3.30	3.50
E1	2.90	3.05	3.20
E2	1.55	1.75	1.95
e	0.65BSC		
H	0.20	0.40	0.57
L	0.06	0.10	0.20
L1	0.30	0.40	0.55
S	10°	12°	14°
M	0.95	1.05	1.15
M1	0.4BSC		

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