

# SSCL028N30GN6

## **N-Channel Enhancement Mode MOSFET**

#### > Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	l <sub>D</sub>
30V	± 20\/	2.8mΩ@10V	1054
	±20V	4.3mΩ@4.5V	105A

# Description

This device is N-Channel enhancement MOSFET.

Uses SGT technology and design to provide excellent

RDSON with low gate charge. This device is suitable
for use in DC-DC conversion, power switch and
charging circuit.

#### 100% UIS + $\Delta VDS$ + Rg Tested!

# Applications

- Motor Drive Control
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

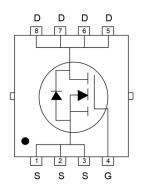
# > Ordering Information

Device	Package	Shipping
SSCL028N30GN6	PDFN5X6-8L	5000/Reel

# Pin Configuration



PDFN5X6-8L (Top View)



**Pin Configuration** 



(XXYY: Internal Traceability Code)



# ➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
$V_{DSS}$	Drain-to-Source Voltage		30	V
V <sub>GSS</sub>	Gate-to-Source Volta	Gate-to-Source Voltage		V
ı	O attack David O and Id	T <sub>C</sub> =25℃	105	^
l <sub>D</sub>	Continuous Drain Current d	T <sub>C</sub> =100℃	67	Α
	Continuous Dunin Comment 3	T <sub>A</sub> =25℃	21	^
IDSM	Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =70°C	17	Α
I <sub>DM</sub>	Pulsed Drain Curren	Pulsed Drain Current <sup>b</sup>		Α
Б	Power Dissipation °	Tc=25°C	69	W
P <sub>D</sub>		T <sub>C</sub> =100℃	27	
Б	Power Dissipation <sup>a</sup>	T <sub>A</sub> =25℃	2.8	34/
P <sub>DSM</sub>		T <sub>A</sub> =70°C	1.8	W
las	Avalanche Current <sup>b</sup> L=0.5mH Single Pulse		24	Α
Eas	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse		144	mJ
TJ	Operation junction temperature		-55~150	°C
T <sub>STG</sub>	Storage temperature range		-55~150	℃

# ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Max.	Unit
Reja	Junction-to-Ambient Thermal Resistance a	44	60	°C/W
$R_{ heta JC}$	Junction-to-Case Thermal Resistance	1.8	2.5	C/VV

#### Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation  $P_D$  is based on  $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

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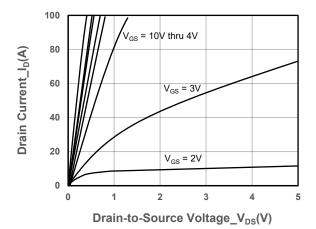
# SSCL028N30GN6

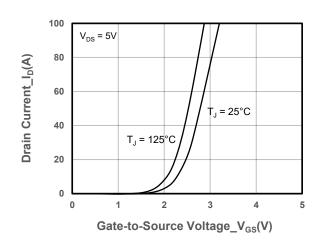
# $\succ$ Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250$ uA	1.0	1.6	2.5	V
Drain Course On Besistance	Б	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		2.8	3.8	- mΩ
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		4.3	5.8	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	Igss	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Transconductance	G <sub>FS</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A		20		S
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A		0.8	1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		0.8		Ω
Input Capacitance	Ciss	V 45V V 0V		1240		
Output Capacitance	Coss	$V_{DS} = 15V, V_{GS} = 0V,$		738		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		32		
Total Gate Charge	Q <sub>G</sub>	10///		24		
Gate to Source Charge	Q <sub>G</sub> s	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V,		5		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 20A		2.6		
Turn-on Delay Time	$T_{D(ON)}$			7		
Rise Time	Tr	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V,		57		]
Turn-off Delay Time	T <sub>D(OFF)</sub>	$I_D = 20A, R_G = 3\Omega$		21		ns
Fall Time	T <sub>f</sub>			14		
Diode Recovery Time	Trr	I <sub>F</sub> =20A, di/dt=100A/us		28		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		19		nC

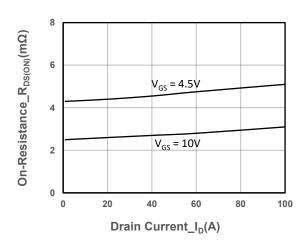


# > Typical Performance Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)

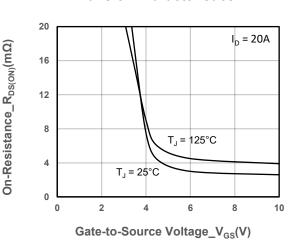




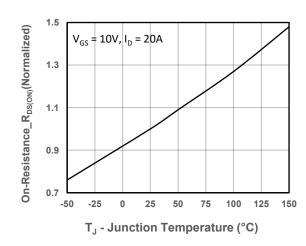
## **Output Characteristics**



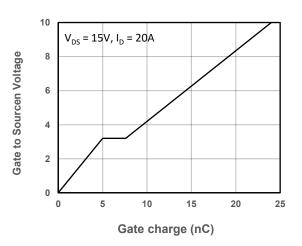
#### **Transfer Characteristics**



## On-Resistance vs. Drain Current and Gate Voltage



## On-Resistance vs. Gate-to-Source Voltage



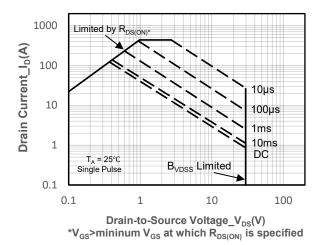
**On-Resistance vs. Junction Temperature** 

Gate-Source Voltage vs. Gate charge

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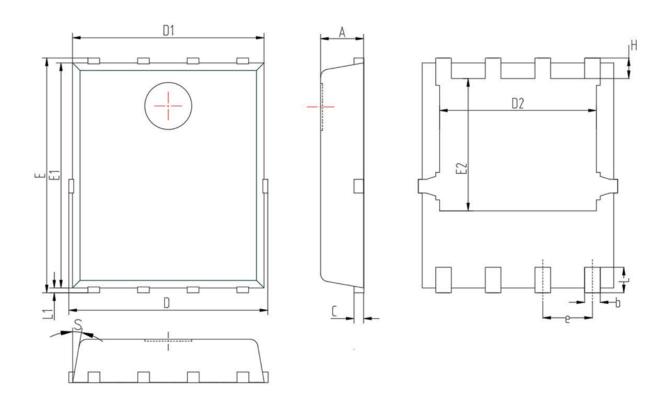
Safe Operating Area vs. Junction-to-Ambient

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# Package Information



Symbol	MILL IMETER			
	Min	Nom	Max	
А	0.90	1.05	1.20	
b	0.25	0.30	0.51	
С	0.15	0.25	0.35	
D	4.80	5.10	5.40	
D1	4.80	5.00	5.20	
D2	3.70	4.00	4.30	
E	5.80	6.15	6.50	
E1	5.50	5.75	5.95	
E2	3.30	3.45	3.67	
е	1.27BSC			
Н	0.40	0.60	0.93	
L	0.45	0.65	0.85	
L1	0.00	0.10	0.25	
S	0°		12°	



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