



SSC8041GN4

P-Channel Enhancement Mode MOSFET

➤ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$	I_D
-40V	$\pm 20V$	9m Ω @-10V	-36A
		16m Ω @-4V5	

➤ Description

This SSC8041GN4 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

100% UIS + ΔV_{DS} + R_g Tested!

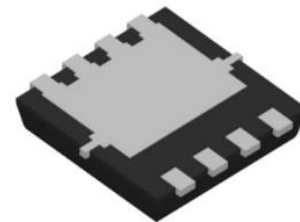
➤ Applications

- Load Switch
- PWM Application
- Power Management

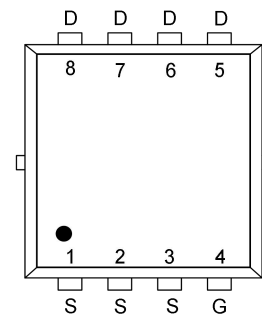
➤ Ordering Information

Device	Package	Shipping
SSC8041GN4	PDFN3.3X3.3-8L	5000/Reel

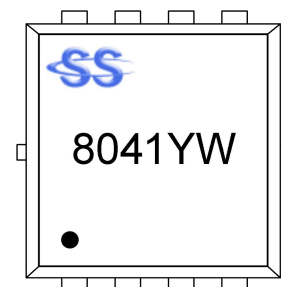
➤ Pin configuration



PDFN3.3X3.3-8L (Bottom View)



Pin Configuration (Top View)



Marking

(YW: Internal Traceability Code)

**➤ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		-40	V
V_{GSS}	Gate-to-Source Voltage		± 20	V
I_{D}	Continuous Drain Current ^d	$T_{\text{C}}=25^{\circ}\text{C}$	-36	A
		$T_{\text{C}}=100^{\circ}\text{C}$	-20.4	
I_{DSM}	Continuous Drain Current ^a	$T_{\text{A}}=25^{\circ}\text{C}$	-14	A
		$T_{\text{A}}=70^{\circ}\text{C}$	-10.7	
I_{DM}	Pulsed Drain Current ^b		-144	A
P_{D}	Power Dissipation ^c	$T_{\text{C}}=25^{\circ}\text{C}$	21	W
		$T_{\text{C}}=100^{\circ}\text{C}$	8.3	
P_{DSM}	Power Dissipation ^a	$T_{\text{A}}=25^{\circ}\text{C}$	3.13	W
		$T_{\text{A}}=70^{\circ}\text{C}$	2	
I_{AS}	Avalanche Current ^b $L=0.5\text{mH}$ Single Pulse		-17	A
E_{AS}	Avalanche Energy ^b $L=0.5\text{mH}$ Single Pulse		72.3	mJ
T_{J}	Operation junction temperature		-55~150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range		-55~150	

➤ Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta\text{JA}}$	Junction-to-Ambient Thermal Resistance ^a	40	$^{\circ}\text{C}/\text{W}$
$R_{\theta\text{JC}}$	Junction-to-Case Thermal Resistance	6	

Note:

- The value of $R_{\theta\text{JA}}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_{D} is based on $T_{\text{J(MAX)}}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

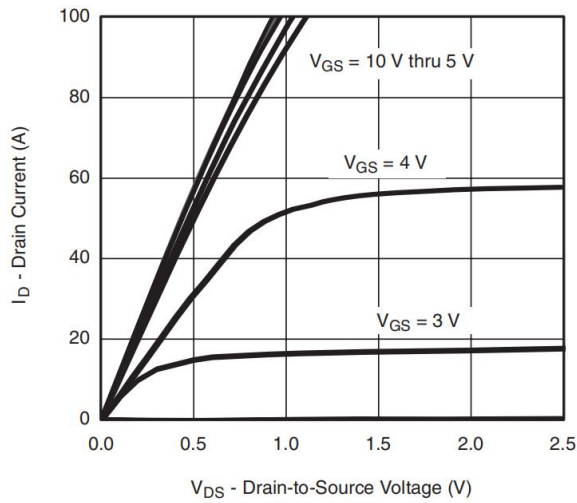


➤ **Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)**

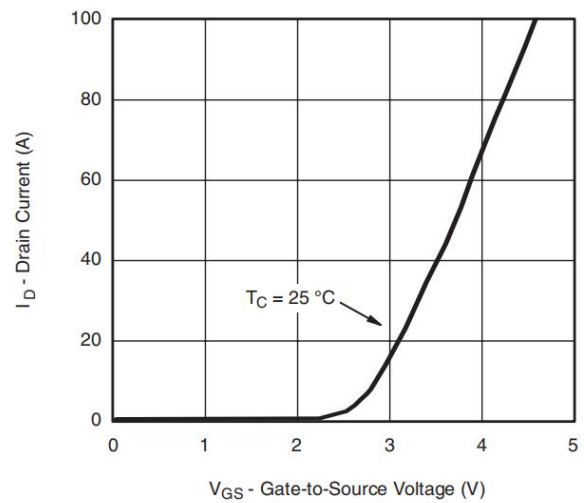
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.2	-2.1	-2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -20A$		9	13	m Ω
		$V_{GS} = -4.5V, I_D = -10A$		16	23	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -40V, V_{GS} = 0V$			-1	μA
Gate-Source Leak Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
Transconductance	G_{FS}	$V_{DS} = -15V, I_D = -12A$		40		s
Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = -5A$			-1.4	V
Gate Resistance	R_G	$V_{DS} = 0V, f = 1MHz$		4		Ω
Input Capacitance	C_{ISS}	$V_{DS} = -20V, V_{GS} = 0V,$ $f = 1MHz$		2500		pF
Output Capacitance	C_{OSS}			250		
Reverse Transfer Capacitance	C_{RSS}			230		
Total Gate Charge	Q_G	$V_{GS} = -10V, V_{DS} = -20V,$ $I_D = -15A$		18		nC
Gate to Source Charge	Q_{GS}			5		
Gate to Drain Charge	Q_{GD}			6		
Turn-on Delay Time	$T_{D(ON)}$	$V_{GS} = -10V, V_{DS} = -10V,$ $R_L = 10\Omega, R_G = 1\Omega,$		12		ns
Rise Time	T_r			12		
Turn-off Delay Time	$T_{D(OFF)}$			23		
Fall Time	T_f			9		
Diode Recovery Time	T_{rr}	$I_F = -20A, di/dt = 500A/\mu s$		20		ns
Diode Recovery Charge	Q_{rr}	$I_F = -20A, di/dt = 500A/\mu s$		18		nC



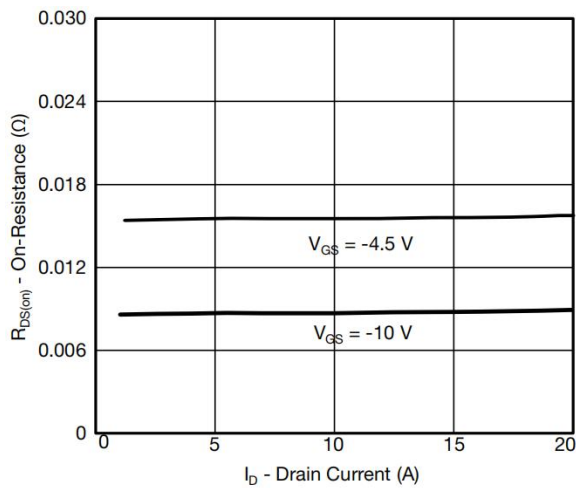
➤ Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)



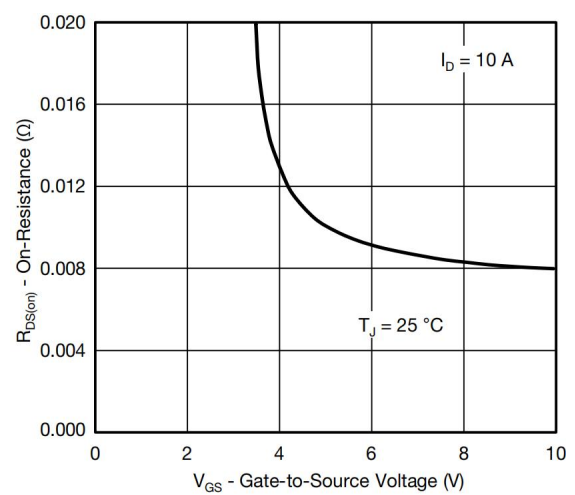
Output Characteristics



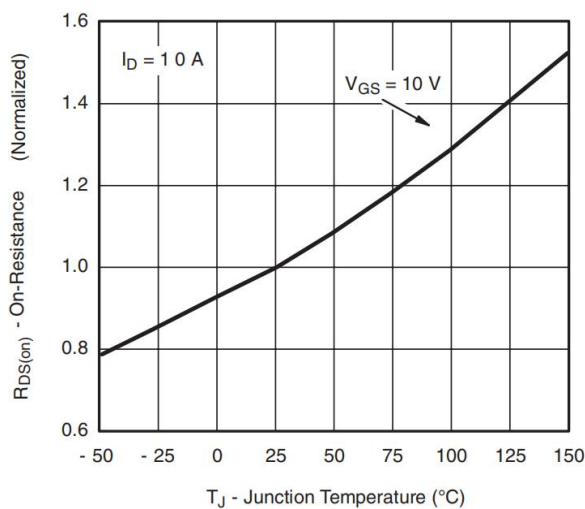
Transfer Characteristics



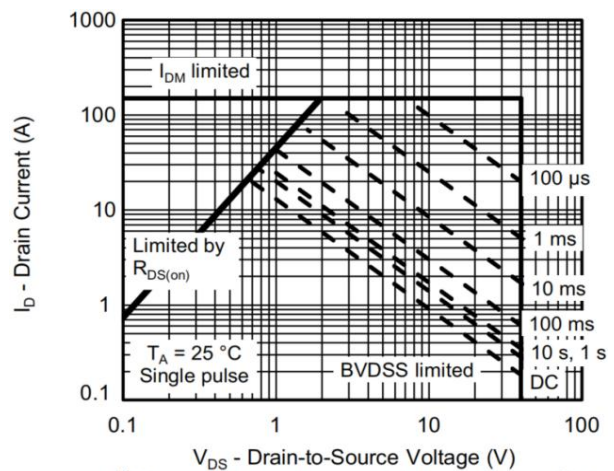
On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage

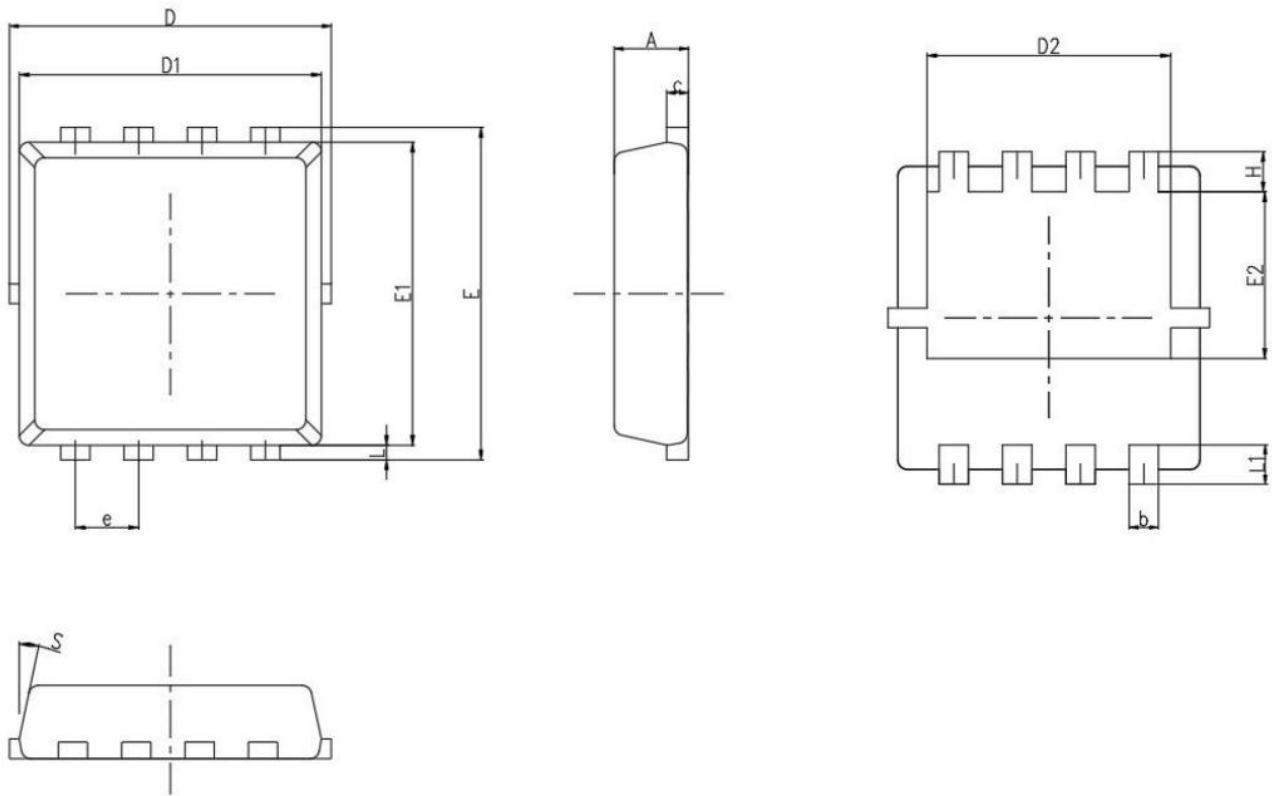


On-Resistance vs. Junction Temperature



Safe Operating Area, Junction-to-Ambient

➤ Package Information



Symbol	MILL IMETER		
	Min	Nom	Max
A	0.65	0.75	0.9
b	0.20	0.3	0.40
c	0.1	/	0.22
D	3.1	3.3	3.45
D1	3	3.15	3.2
D2	2.55	2.5	2.75
E	3.15	3.3	3.45
E1	2.9	3.05	3.2
E2	1.55	1.75	1.95
e	0.65BSC		
L	0.06	0.15	0.2
L1	0.25	0.4	0.55
H	0.31	0.35	0.6
S	10°	12°	14°



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