



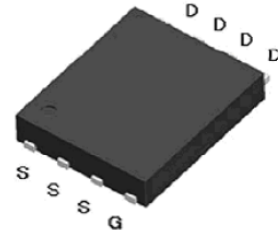
## SSC80312GN6

### N-Channel Enhancement Mode MOSFET

#### ➤ Features

$V_{DS}$	$V_{GS}$	$R_{DS(ON)}$ Typ.	$I_D$
30V	$\pm 20V$	3m $\Omega$ @10V	90A
		4.2m $\Omega$ @4.5V	

#### ➤ Pin configuration

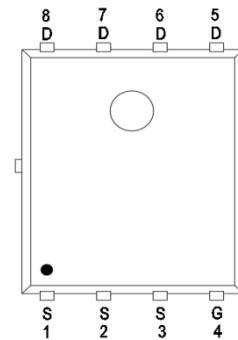


**PDFN5X6-8L(Top View)**

#### ➤ Description

The SSC80312GN6 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

**100% UIS +  $\Delta V_{DS}$  +  $R_g$  Tested!**



**Pin Configuration**

#### ➤ Applications

- PWM Applications
- Load Switch
- DC-DC Converters
- Wireless Chargers

#### ➤ Ordering Information

Device	Package	Shipping
SSC80312GN6	PDFN5X6-8L	5000/Reel



**Marking**

(XXYY: Internal Traceability Code)

**➤ Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter		Ratings	Unit
$V_{\text{DSS}}$	Drain-to-Source Voltage		30	V
$V_{\text{GSS}}$	Gate-to-Source Voltage		$\pm 20$	V
$I_{\text{D}}$	Continuous Drain Current <sup>d</sup>	$T_{\text{C}}=25^{\circ}\text{C}$	90	A
		$T_{\text{C}}=100^{\circ}\text{C}$	48	
$I_{\text{DSM}}$	Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}\text{C}$	29	A
		$T_A=70^{\circ}\text{C}$	21	
$I_{\text{DM}}$	Pulsed Drain Current <sup>b</sup>		360	A
$P_{\text{D}}$	Power Dissipation <sup>c</sup>	$T_{\text{C}}=25^{\circ}\text{C}$	39	W
		$T_{\text{C}}=100^{\circ}\text{C}$	15.7	
$P_{\text{DSM}}$	Power Dissipation <sup>a</sup>	$T_A=25^{\circ}\text{C}$	4.2	W
		$T_A=70^{\circ}\text{C}$	2.7	
$I_{\text{AS}}$	Avalanche Current <sup>b</sup> $L=0.5\text{mH}$ Single Pulse		25	A
$E_{\text{AS}}$	Avalanche Energy <sup>b</sup> $L=0.5\text{mH}$ Single Pulse		156	mJ
$T_{\text{J}}$	Operation junction temperature		-55~150	$^{\circ}\text{C}$
$T_{\text{STG}}$	Storage temperature range		-55~150	

**➤ Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Ratings	Max.	Unit
$R_{\theta\text{JA}}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	30	39	$^{\circ}\text{C}/\text{W}$
$R_{\theta\text{JC}}$	Junction-to-Case Thermal Resistance	3.2	4.5	

Note:

- The value of  $R_{\theta\text{JA}}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design. The current rating is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_{\text{D}}$  is based on  $T_{\text{J(MAX)}}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

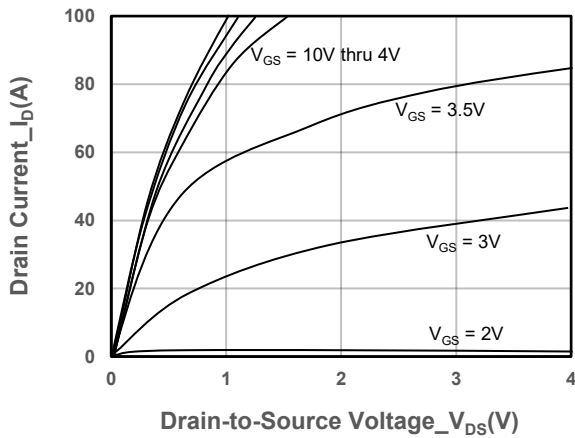


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

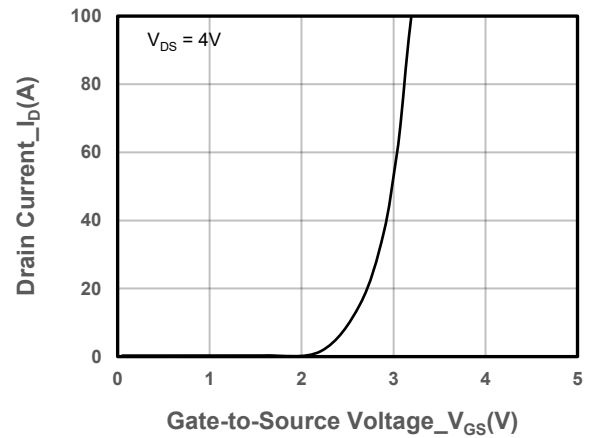
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1	1.5	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		3	4.2	mΩ
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		4.2	6	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V			1	uA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A		0.8	1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		2.7		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz		2315		pF
Output Capacitance	C <sub>OSS</sub>			286		
Reverse Transfer Capacitance	C <sub>RSS</sub>			235		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A		43		nC
Gate to Source Charge	Q <sub>GS</sub>			8		
Gate to Drain Charge	Q <sub>GD</sub>			11		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A, R <sub>G</sub> = 3Ω		8.5		ns
Rise Time	T <sub>r</sub>			15		
Turn-off Delay Time	T <sub>D(OFF)</sub>			33		
Fall Time	T <sub>f</sub>			12		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		13		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		3.4		nC



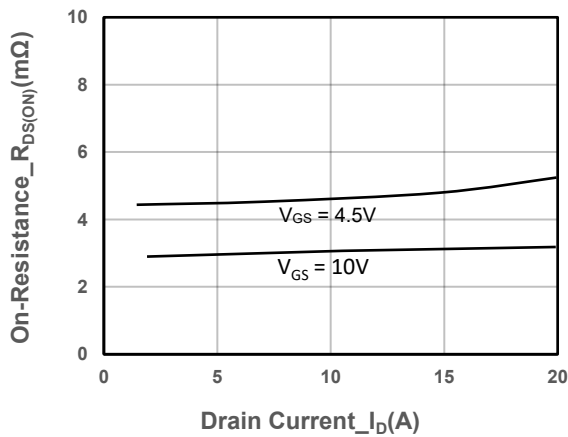
## ➤ Typical Performance Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)



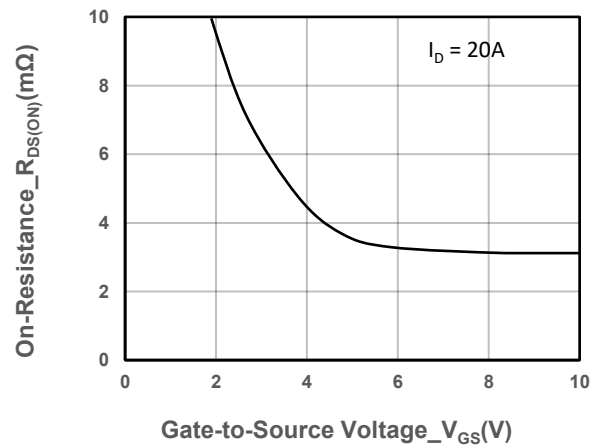
Output Characteristics



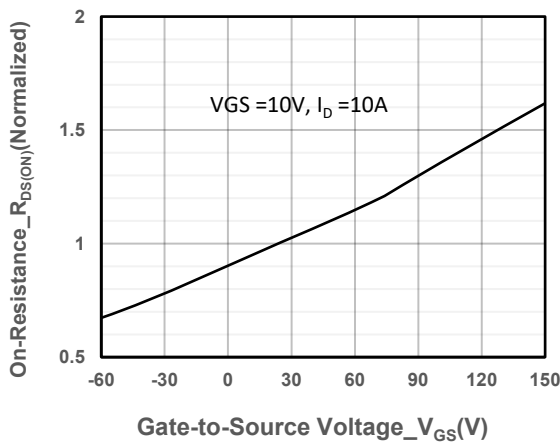
Transfer Characteristics



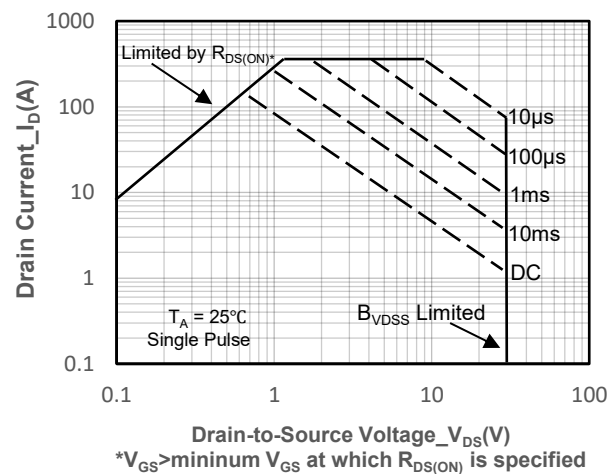
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage

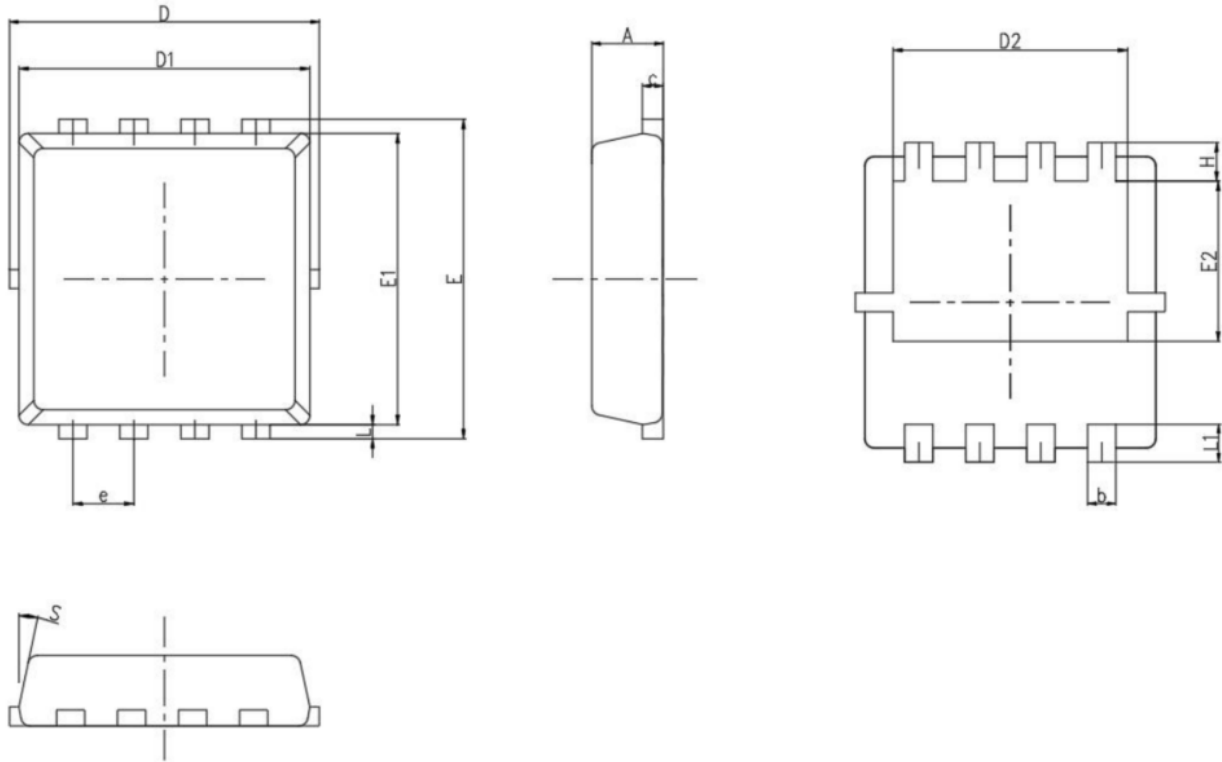


On-Resistance vs. Junction Temperature



Safe Operating Area vs. Junction-to-Ambient

## ➤ Package Information



Symbol	MILL IMETER		
	Min	Nom	Max
A	0.65	0.75	0.9
b	0.20	0.3	0.40
c	0.1	/	0.22
D	3.1	3.3	3.45
D1	3	3.15	3.2
D2	2.55	2.5	2.75
E	3.15	3.3	3.45
E1	2.9	3.05	3.2
E2	1.55	1.75	1.95
e	0.65BSC		
L	0.06	0.15	0.2
L1	0.25	0.4	0.55
H	0.31	0.35	0.6
S	10°	12°	14°



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