

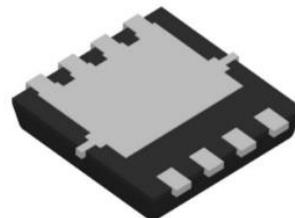
SSC8066GN4

N-Channel Enhancement Mode MOSFET

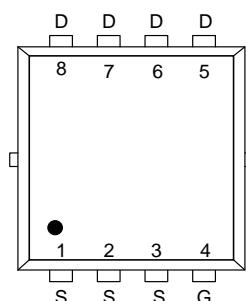
➤ Features

V _{DS}	V _{GS}	R _{DSON} (Typ.)	I _D
60V	$\pm 20V$	13m Ω @10V	36A
		19m Ω @4V5	

➤ Pin configuration



PDFN3.3X3.3-8L (Bottom View)



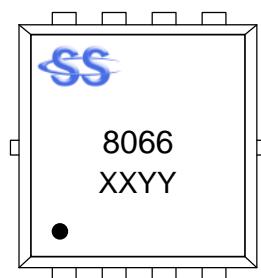
Pin Configuration (Top View)

➤ Applications

- Load Switch
- PWM Application
- Power Management

➤ Ordering Information

Device	Package	Shipping
SSC8066GN4	PDFN3.3X3.3-8L	5000/Reel



Marking

(XXYY: Internal Traceability Code)

➤ Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	60	V
V_{GSS}	Gate-to-Source Voltage	±20	V
I_D	Continuous Drain Current ^d	$T_C=25^\circ\text{C}$	36
		$T_C=100^\circ\text{C}$	19
I_{DSM}	Continuous Drain Current ^a	$T_A=25^\circ\text{C}$	12
		$T_A=70^\circ\text{C}$	8.8
I_{DM}	Pulsed Drain Current ^b	142	A
P_D	Power Dissipation ^c	$T_C=25^\circ\text{C}$	27
		$T_C=100^\circ\text{C}$	11
P_{DSM}	Power Dissipation ^a	$T_A=25^\circ\text{C}$	2.8
		$T_A=70^\circ\text{C}$	1.8
I_{AS}	Avalanche Current ^b L=0.5mH Single Pulse	16	A
E_{AS}	Avalanche Energy ^b L=0.5mH Single Pulse	64	mJ
T_J	Operation junction temperature	-55~150	$^\circ\text{C}$
T_{STG}	Storage temperature range	-55~150	

➤ Thermal Resistance Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	35	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	4.4	

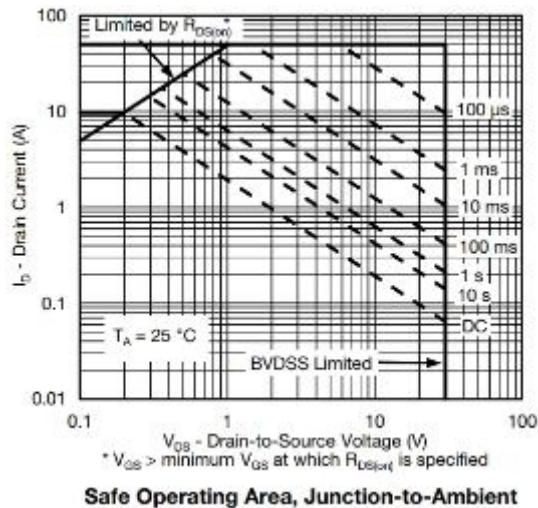
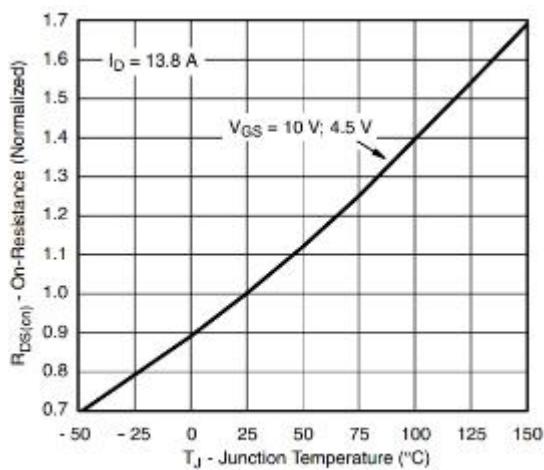
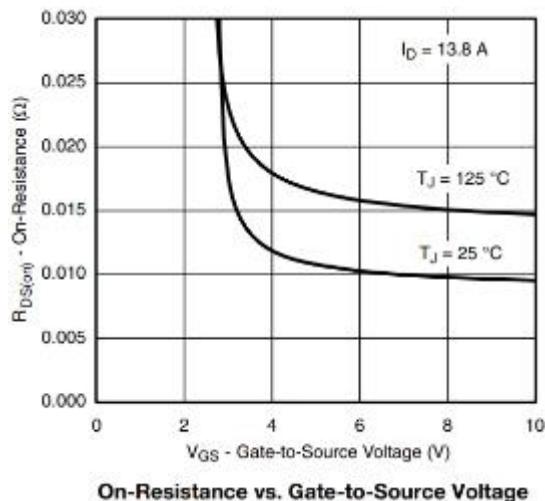
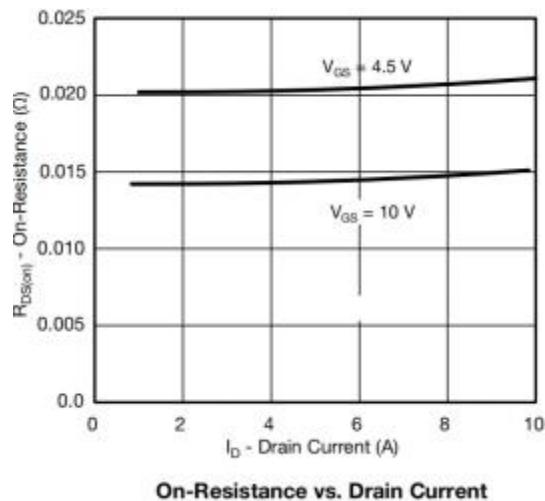
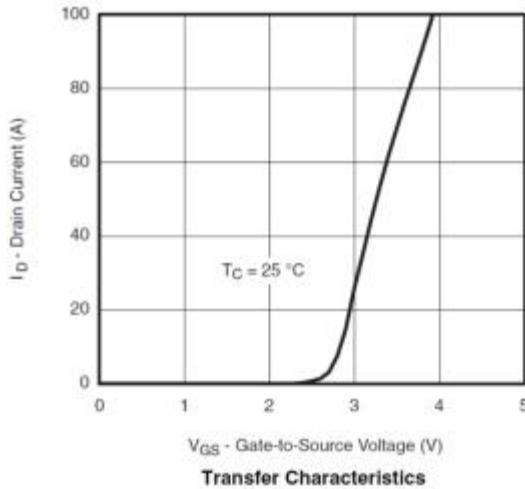
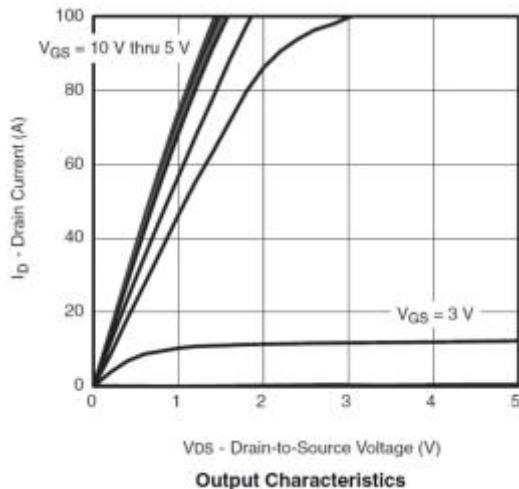
Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

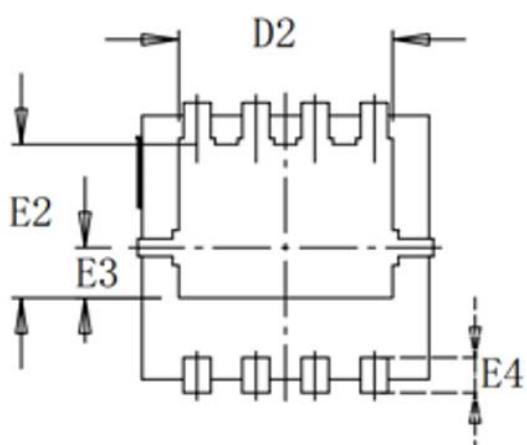
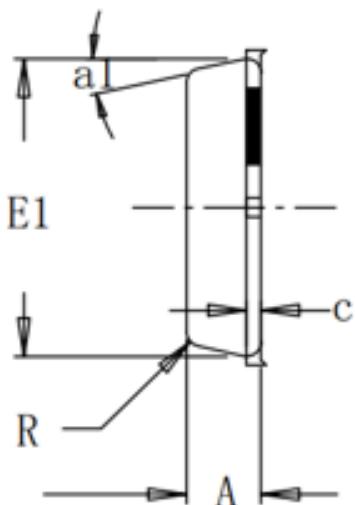
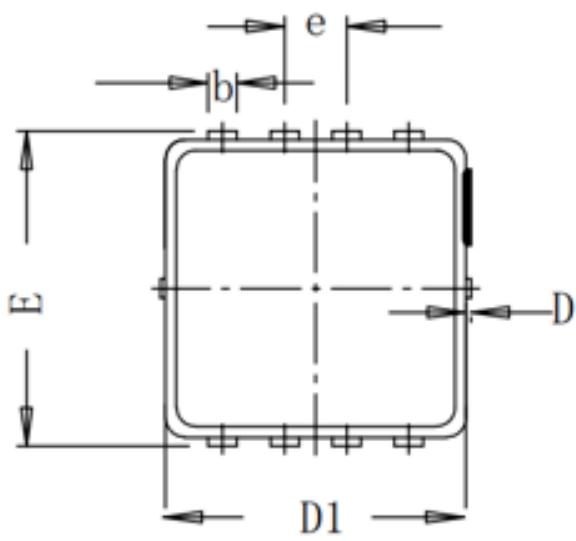
➤ Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.4	1.9	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 9A$		13	20	$m\Omega$
		$V_{GS} = 4.5V, I_D = 6A$		19	29	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60V, V_{GS} = 0V$			1	μA
Gate-Source Leak Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
Transconductance	G_{FS}	$V_{DS} = 5V, I_D = 10A$		16		s
Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = 9A$			1.4	V
Gate Resistance	R_G	$V_{DS} = 0V, f = 1MHz$		1.4	2.2	Ω
Input Capacitance	C_{ISS}	$V_{DS} = 15V, V_{GS} = 0V,$ $f = 1MHz$		1070		pF
Output Capacitance	C_{OSS}			108		
Reverse Transfer Capacitance	C_{RSS}			86		
Total Gate Charge	Q_G	$V_{GS} = 10V, V_{DS} = 30V,$ $I_D = 15A$		18		nC
Gate to Source Charge	Q_{GS}			9		
Gate to Drain Charge	Q_{GD}			6		
Turn-on Delay Time	$T_{D(ON)}$	$V_{GS} = 10V, V_{DS} = 10V, R_L$ $= 10\Omega, R_G = 1\Omega$		9		ns
Rise Time	T_r			4		
Turn-off Delay Time	$T_{D(OFF)}$			15		
Fall Time	T_f			6		
Diode Recovery Time	T_{rr}	$I_F=20A, di/dt=500A/us$		12		ns
Diode Recovery Charge	Q_{rr}	$I_F=20A, di/dt=500A/us$		19		nC

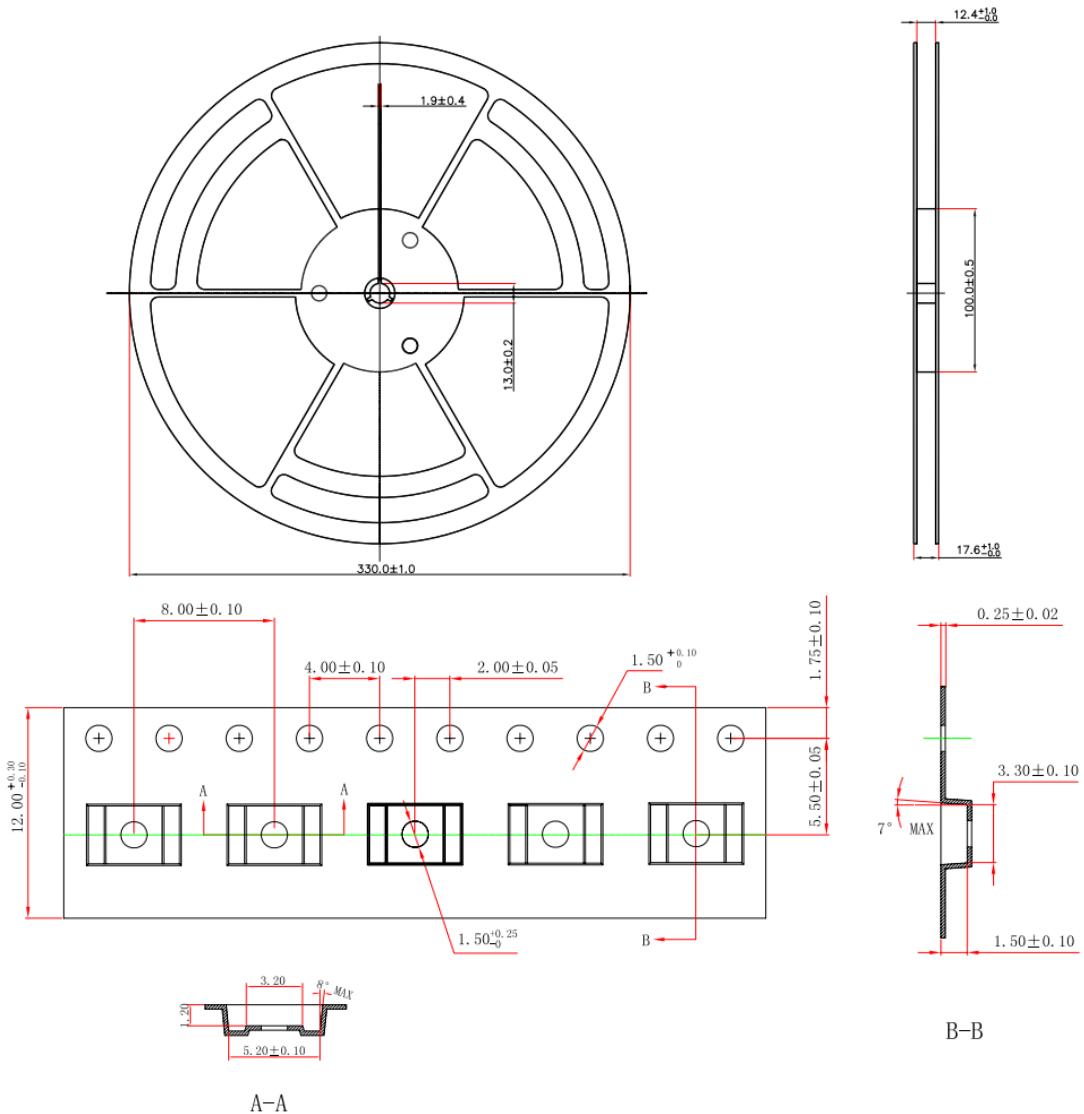
➤ Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)



➤ Package Information



Dim	Millimeters		
	Min.	Nom.	Max.
A	0.75	0.78	0.81
b	0.297	0.3	0.35
c	-	0.152	-
D	0	0.05	0.1
D1	3.12	3.15	3.18
D2	-	2.35	-
E	3.2	3.3	3.4
E1	3.09	3.12	3.15
E2	-	1.75	-
E3	-	0.575	-
E4	-	0.4	-
R	-	0.15	-
e	0.65BSC		
a1°	-	12°	-

➤ **Tape and Reel**


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