

### SSC8166GN4

#### **N-Channel Enhancement Mode MOSFET**

#### Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	l <sub>D</sub>
60V	±20V	31mΩ@10V	22A
		34mΩ@4V5	22A

### Description

This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package.

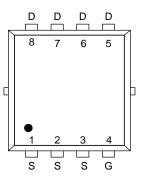
#### Applications

- Load Switch
- Portable Devices
- DCDC Conversion
- Charging

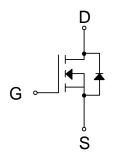
### Ordering Information

Device	Package	Shipping
SSC8166GN4	PDFN3.3X3.3-8L	5000/Reel

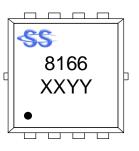
### > Pin Configuration



PDFN3.3X3.3-8L (Top View)



**Pin Configuration** 



**Marking** 

(XXYY: Internal Traceability Code)



## ➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit		
Drain-to-Source Voltage	V <sub>DSS</sub>	60	V		
Gate-to-Source Voltage		V <sub>GSS</sub>	±20	V	
Continuous Proin Current	Tc=25℃	1_	22	۸	
Continuous Drain Current d Tc=100°C		- I <sub>D</sub>	12	A	
Continuous Drain Current a	T <sub>A</sub> =25℃		7	Α	
Continuous Drain Current "	T <sub>A</sub> =70°C	IDSM	5		
Pulsed Drain Current <sup>b</sup>	•	Ірм	80	Α	
Payer Dissipation 6	T <sub>C</sub> =25℃	В	25	W	
Power Dissipation <sup>c</sup>	Tc=100°C	P <sub>D</sub>	10	]	
Payer Dissipation 3	T <sub>A</sub> =25℃	В	3.1	W	
Power Dissipation <sup>a</sup>	T <sub>A</sub> =70°C	- P <sub>DSM</sub>	2	VV	
Operation junction temperature	TJ	-55~150	- °C		
Storage temperature range	T <sub>STG</sub>	-55~150			

## ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance <sup>a</sup>	Reja	40	52	°C/W
Junction-to-Case Thermal Resistance	$R_{ heta JC}$	5	6.5	C/VV

#### Note:

- a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A$ =25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation  $P_D$  is based on  $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

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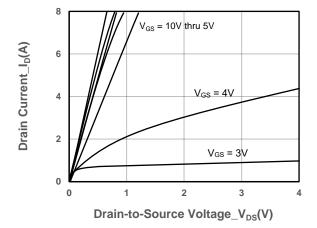


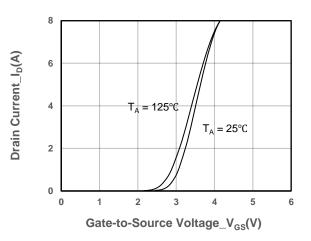
# $\succ$ Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250$ uA	1	1.6	3	V
Drain Course On Registeres	Б	$V_{GS} = 10V, I_D = 7A$ $V_{GS} = 4.5V, I_D = 5A$		31	37	mΩ
Drain-Source On-Resistance	RDS(on)			34	43	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	Igss	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2A		0.75	1.3	V
Input Capacitance	Cıss	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V,		1210		
Output Capacitance	Coss			65		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		53		
Total Gate Charge	$Q_{\mathrm{G}}$	\\ -45\\\\ -45\\\		9		
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 15V,		3.1		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 5A		3		
Turn-on Delay Time	T <sub>D(ON)</sub>	$V_{GS} = 4.5V, V_{DS} = 10V,$ $R_{L} = 1.4\Omega, R_{G} = 6\Omega,$ $I_{D} = 5A$		6		
Rise Time	Tr			6		
Turn-off Delay Time	$T_{D(OFF)}$			32		ns
Fall Time	T <sub>f</sub>	ID – SA		11		



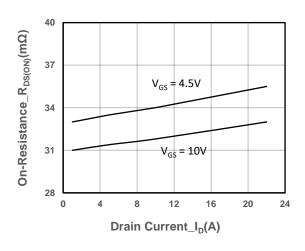
## ➤ Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

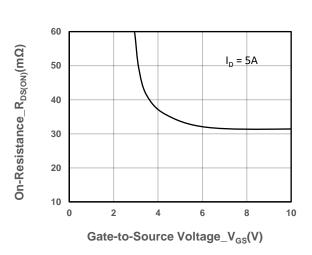




#### **Output Characteristics**

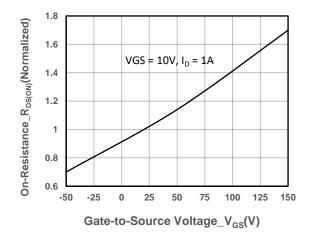


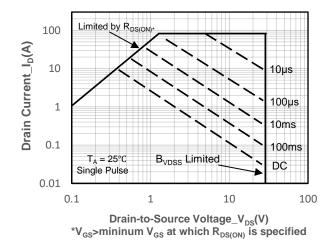




### On-Resistance vs. Drain Current and Gate Voltag

On-Resistance vs. Gate-to-Source Voltage



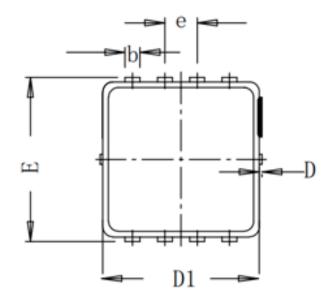


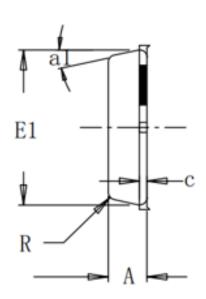
**On-Resistance vs. Junction Temperature** 

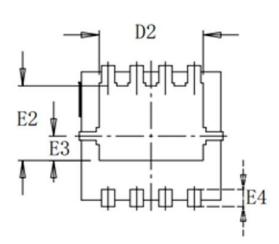
Safe Operating Area vs. Junction-to-Ambient



## > Package Information







Cumbal	Dimensions In Millimeters				
Symbol	Min.	Nom.	Max.		
Α	0.75	0.78	0.81		
b	0.297	0.3	0.35		
С	-	0.152	-		
D	0	0.05	0.1		
D1	3.12	3.15	3.18		
D2	-	2.35	-		
E	3.2	3.3	3.4		
E1	3.09	3.12	3.15		
E2	-	1.75	=		
E3	-	0.575	-		
E4	-	0.4	-		
R	-	0.15 -			
е	0.65BSC				
a1°	-	12° -			



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