

SSC8066GN6

N-Channel Enhancement Mode MOSFET

Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	Ι _D
60V	+20V	13mΩ@10V	42A
	<u> </u>	18mΩ@4V5	42/1

> Description

This SSC8066GN6 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

100% UIS + ΔVDS + Rg Tested!

Applications

- Load Switch
- PWM Application
- Power Management

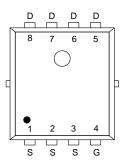
Ordering Information

Device	Package	Shipping
SSC8066GN6	PDFN5X6-8L	5000/Reel

Pin configuration



PDFN5X6-8L



Pin Configuration (Top View)



Marking

(XXYY: Internal Traceability Code)



Absolute Maximum Ratings (T_A=25[°]C unless otherwise noted)

Symbol	Parameter	Ratings	Unit		
V_{DSS}	Drain-to-Source Volta	Drain-to-Source Voltage		V	
V _{GSS}	Gate-to-Source Volta	Gate-to-Source Voltage		V	
	Continuous Brain Comment d	T _C =25℃	42	^	
l _D	Continuous Drain Current ^d	T _C =100°C	24	Α	
	Continuous Brain Comment 2	T _A =25℃	14	Δ.	
IDSM	Continuous Drain Current ^a	T _A =70°C	10	A	
I _{DM}	Pulsed Drain Curren	Pulsed Drain Current ^b		А	
Б	5	Tc=25℃	43	10/	
P _D	Power Dissipation ^c	T _C =100°C	17	W	
Б	Power Dissipation ^a	T _A =25℃	4.1	10/	
P _{DSM}		T _A =70°C	2.6	W	
I _{AS}	Avalanche Current b L=0.5mH Single Pulse		16	А	
Eas	Avalanche Energy ^b L=0.5mH Single Pulse		64	mJ	
TJ	Operation junction temperature		-55~150	°C	
T _{STG}	Storage temperature range		-55~150		

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance a	30	°C/W
R ₀ JC	Junction-to-Case Thermal Resistance	2.9	C/ VV

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

SSC-V1.0 www.sscsemi.com Analog Future



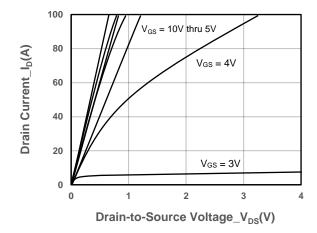


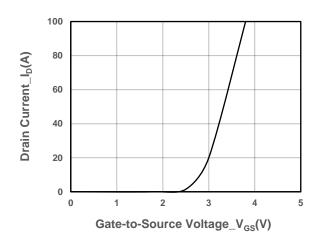
> Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250µA	60			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250uA$	1.4	1.9	2.5	٧
Drain Course On Registeres	D	V _{GS} = 10V, I _D = 9A		13	20	0
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 4.5V, I _D = 6A		18	29	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	Igss	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = 5V, I _D = 10A		16		s
Forward Voltage	V _{SD}	V _G S = 0V, I _S = 9A			1.4	V
Gate Resistance	R _G	$V_{DS} = 0V, f = 1MHz$		1.4	2.2	Ω
Input Capacitance	Ciss	V 45V V 0V		1700		
Output Capacitance	Coss	$V_{DS} = 15V$, $V_{GS} = 0V$,		110		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		92		
Total Gate Charge	Q _G	V 40V V 20V		18		
Gate to Source Charge	Q _{GS}	$V_{GS} = 10V, V_{DS} = 30V,$		9		nC
Gate to Drain Charge	Q _{GD}	I _D = 15A		6		
Turn-on Delay Time	T _{D(ON)}			9		
Rise Time	Tr	V _{GS} = 10V, V _{DS} = 10V,		4		
Turn-off Delay Time	T _{D(OFF)}	$R_L = 10\Omega$, $R_G = 1\Omega$		15		ns
Fall Time	Tf			6		
Diode Recovery Time	Trr	I _F =20A, di/dt=500A/us		12		ns
Diode Recovery Charge	Q _{rr}	I _F =20A, di/dt=500A/us		19		nC

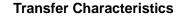


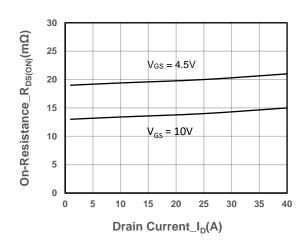
> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)

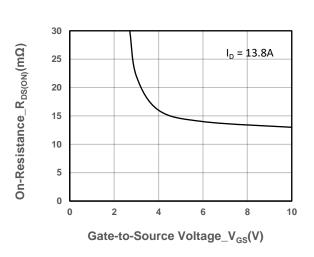




Output Characteristics

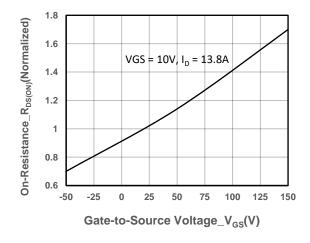


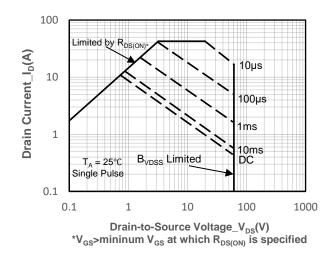




On-Resistance vs. Drain Current and Gate Voltag

On-Resistance vs. Gate-to-Source Voltage



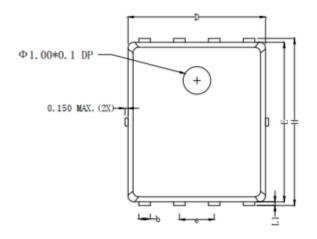


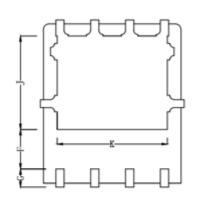
On-Resistance vs. Junction Temperature

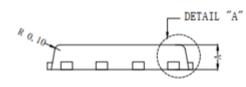
Safe Operating Area vs. Junction-to-Ambient

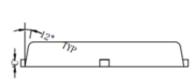


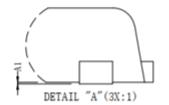
Package Information











Complete	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
Α	0.90	1.00	1.10	
A1	0.00	0.03	0.05	
b	0.25	0.03	0.35	
С	0.254 REF			
D	4.80	4.90	5.00	
F	1.35 REF			
E	5.65	5.75	5.85	
е	1.27 BSC			
Н	5.90	6.00	6.10	
L1	0.10	0.13	0.16	
G	0.55 REF			
K	4.00 REF			
J	3.45 REF			



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