

# SSC80310GN4

#### **N-Channel Enhancement Mode MOSFET**

#### > Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub>	l <sub>D</sub>
30V	1.201/	9mΩ@10V	424
	±20V	13mΩ@4.5V	43A

#### > Description

This device uses advanced trench technology to provide excellent RDSON and low gate charge. This device is suitable for use as a load switch or in PWM applications.

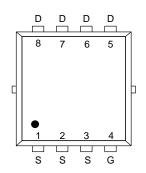
## Applications

- Load Switch
- NB/PC
- DCDC conversion

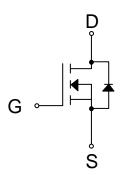
#### > Ordering Information

Device	Package Shipping	
SSC80310GN4	PDFN3.3X3.3-8L	5000/Reel

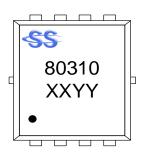
# > Pin Configuration



PDFN3.3X3.3-8L (Top View)



**Pin Configuration** 



**Marking** 

(XXYY: Internal Traceability Code)



### ➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V <sub>DSS</sub>	Drain-to-Source Voltage		30	V
V <sub>GSS</sub>	Gate-to-Source Volta	Gate-to-Source Voltage		V
	Continuous Drain Current <sup>d</sup>	T <sub>C</sub> =25℃	43	Δ.
l <sub>D</sub>		T <sub>C</sub> =100℃	24	Α
	Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25℃	16	
IDSM		T <sub>A</sub> =70°C	11.7	Α
Ірм	Pulsed Drain Curren	172	Α	
5	Power Dissipation <sup>c</sup>	Tc=25℃	27	10/
P <sub>D</sub>		T <sub>C</sub> =100℃	11.1	W
	Power Dissipation <sup>a</sup>	T <sub>A</sub> =25℃	3.7	10/
P <sub>DSM</sub>		T <sub>A</sub> =70°C	2.3	W
las	Avalanche Current <sup>b</sup> L=0.5mH	10	Α	
Eas	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse		25	mJ
TJ	Operation junction temperature		-55~150	°C
T <sub>STG</sub>	Storage temperature ra	-55~150	$^{\circ}$	

#### ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
RθJA	Junction-to-Ambient Thermal Resistance a	34	44	°C/W
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	4.5	5.8	C/VV

#### Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation  $P_D$  is based on  $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.



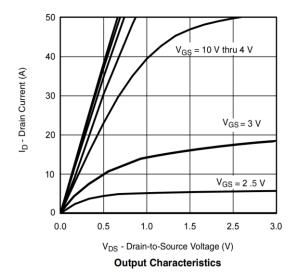
# SSC80310GN4

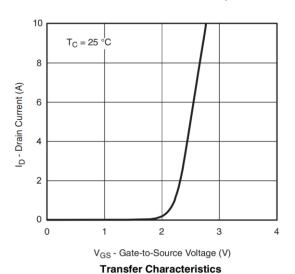
# $\succ$ Electrical Characteristics (T<sub>A</sub>=25 $^{\circ}$ C unless otherwise noted)

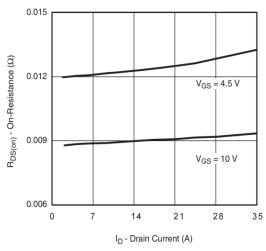
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1	1.5	2.5	V
Drain Course On Besistance	D	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		9	12	- mΩ
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		13	18	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V			1	μΑ
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Transconductance	G <sub>FS</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A		13		s
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A		0.8	1.3	V
Gate Resistance	Rg	f=1MHZ		2.1		Ω
Input Capacitance	Ciss	\\ -45\\\\ -0\\		826		
Output Capacitance	Coss	$V_{DS} = 15V, V_{GS} = 0V,$		100		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		84		
Total Gate Charge	Q <sub>G</sub>	\\ -40\\\\ -45\\		16.7		
Gate to Source Charge	Q <sub>G</sub> s	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V,		2.4		nC
Gate to Drain Charge	Q <sub>GD</sub>	- I <sub>D</sub> =10A		2.7		
Turn-on Delay Time	T <sub>D(ON)</sub>			5.1		
Rise Time	Tr	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V,		6.4		
Turn-off Delay Time	$T_{D(OFF)}$	$R_L = 0.75\Omega$ , $R_G = 3\Omega$		17		ns
Fall Time	Tf			5.9		
Diode Recovery Time	Trr	I <sub>F</sub> =20A, di/dt=100A/us		8		ns
Diode Recovery Charge	Qrr	I <sub>F</sub> =20A, di/dt=100A/us		11		nC

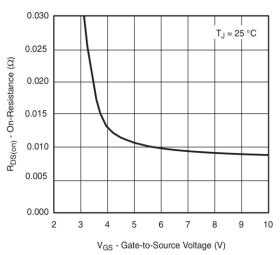


## > Typical Performance Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)



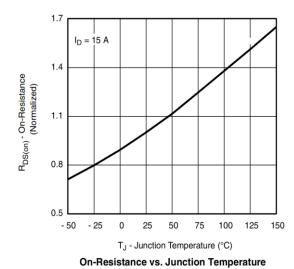


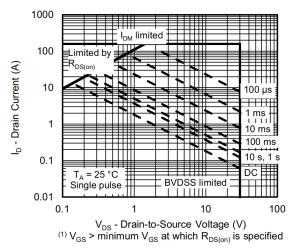










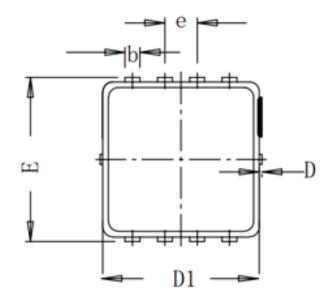


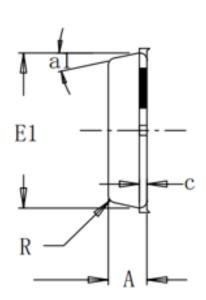
Safe Operating Area, Junction-to-Ambient

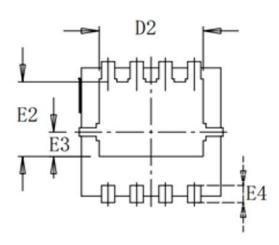
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# Package Information







Cumbal	Dimensions In Millimeters				
Symbol	Min.	Nom.	Max.		
Α	0.75	0.78	0.81		
b	0.297	0.3	0.35		
С	-	0.152	•		
D	0	0.05	0.1		
D1	3.12	3.15	3.18		
D2	-	2.35	-		
E	3.2	3.3	3.4		
E1	3.09	3.12	3.15		
E2	-	1.75	-		
E3	-	0.575	-		
E4	-	0.4	-		
R	-	0.15	•		
е	0.65BSC				
a1°	-	12°	-		



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